

Model Name: GA-H97M-D3H

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU LGA1150-A
05	CPU LGA1150-B
06	CPU LGA1150-C
07	DDR III CHANNEL A 1,2
08	DDR III CHANNEL B 1,2
09	PCH FDI,DMI,USB,PCIE,NVRAM
10	PCH DP,CLK BUFFER
11	PCH HOST,SATA,PCI
12	PCH GPIO,CTRL,AUDIO
13	PCH PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*4 SLOT
16	PCI SLOT1,2
17	ITE 8620 LPC IO
18	COM,KB MS USB,USB30 20
19	HWM,FAN CTRL,OV,-PROCHOT
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	Realtek ALC892-GR
23	REAR AUDIO JACK
24	REALTEK RTL8111F
25	DISCRETE POWER
26	ATX ,TPM
27	VCORE ISL95820 1

SHEET

TITLE

28	VCORE ISL95820 2
29	RT8120 DDR POWER
30	LPT, M3 POWER
31	DVI, HDMI
32	IT8892E

Gigabyte Technology

Cover Sheet

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Revision 1.0

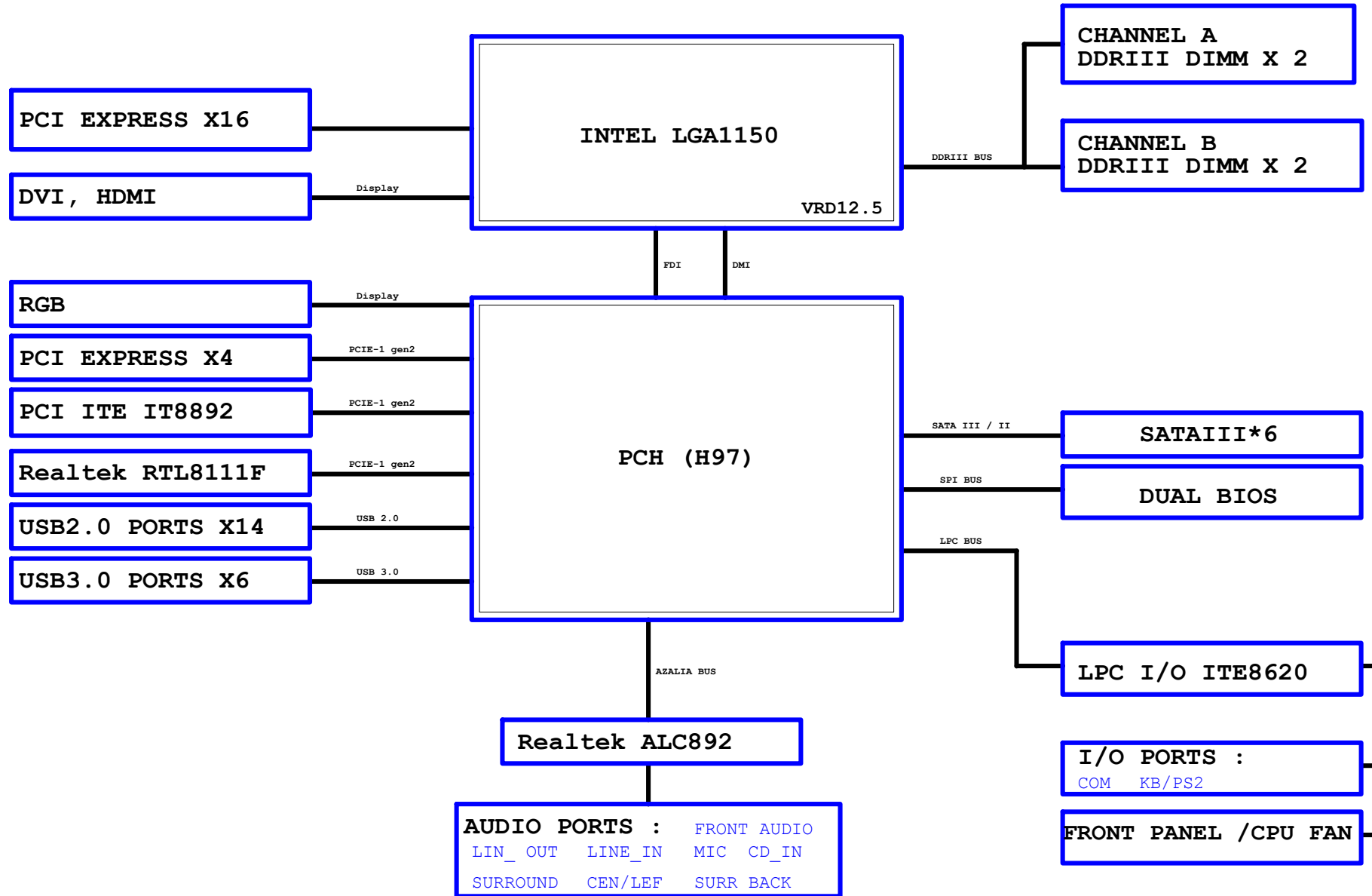
Component value change history

2014/04/25

Data	Change Item	Reason
2013/12/09	機構變更MOS_HS尺寸:長度大小由89修改為79mm,孔大小由D3mm修改為D4mm	
2013/12/11	MR.LIN:移除1 PORT 1 FUSE規格	
2013/12/12	MR.LIN:移除DVI LEVEL SHIFTER改成COSTDOWN設計方式	
2013/12/20	HDD LED/FUSB3.0 ESD PROTECTOR	
2013/12/24	MODIFY AP NOTE	
2013/12/26	MODIFY AP NOTE:USB防燒,IT8620斜插ISSUE	
2013/12/27	R0.1 GERBER OUT	
2014/1/13	AP NOTE:DVI LEVEL SHIFTER改回	
	PCH_HS,MOS_HS:9 SERIES	
	加回AP431 BOM VCC1_5_PCH_OV	
2014/1/16	AP NOTE(UATX):DVI LEVEL SHIFTER移除	BIOS DRIVING 800mV 2dB
	BIOS_PH移除	
2014/1/27	COSTDOWN:5VDUAL-->FUSEVCC_R2,DEL UD7	BAT54A
2014/1/28	AP NOTE:移除F_USB保護線路及AP431	
2014/2/10	CPU FAN PIN2增加C319 0.1u/4/X7R/16V/K	
	Q47,Q48:2N7002 GATE~VCC3	
	FOOT MASK	
2014/2/18	C136:0.1u/6/X7R/25V/K	
	H97 Vcore High /low side ON: 10IF9-584081-00R NTMFS4C08NT1G	
	Non-Vcore High /low side ON: 10IF9-070410-00R NTMFS4C10NT1G	
2014/2/19	R1.0 GERBER OUT	
2014/04/11	Update H97 Chipset 料號 [10HB1-030H97-20R]	
2014/04/25	Update DDR RC	PBOM: 9MH97MD3H-00-10C
	R396: 27K -> 20K	
	R657: 487 -> 680	
	R380: 2.26K -> 2.15K	

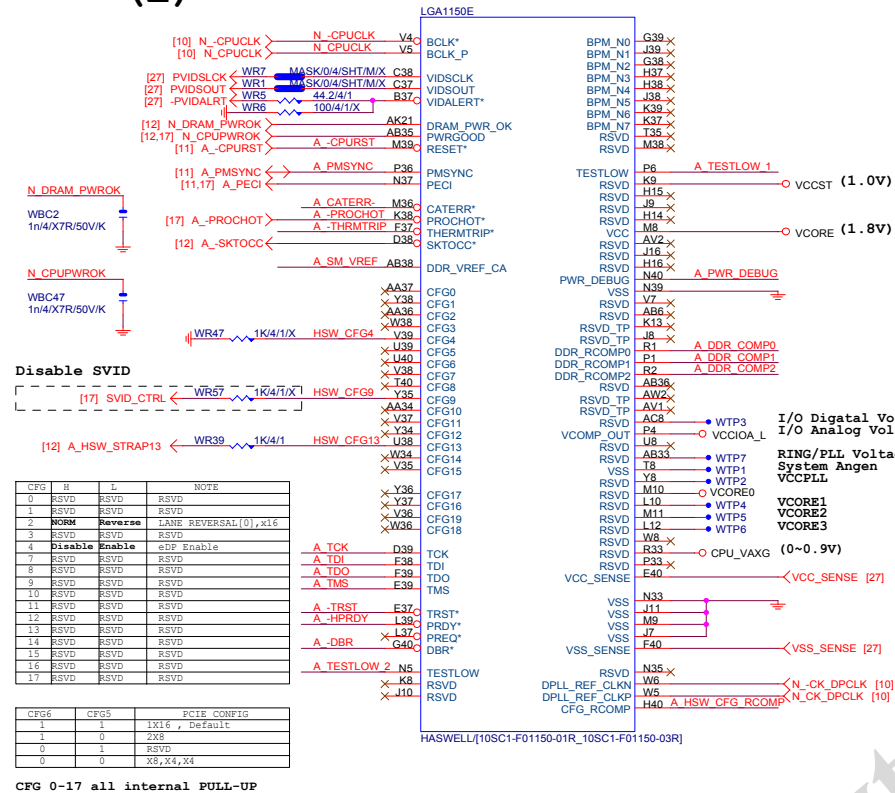
Circuit or PCB layout change

[illegible]

BLOCK DIAGRAM

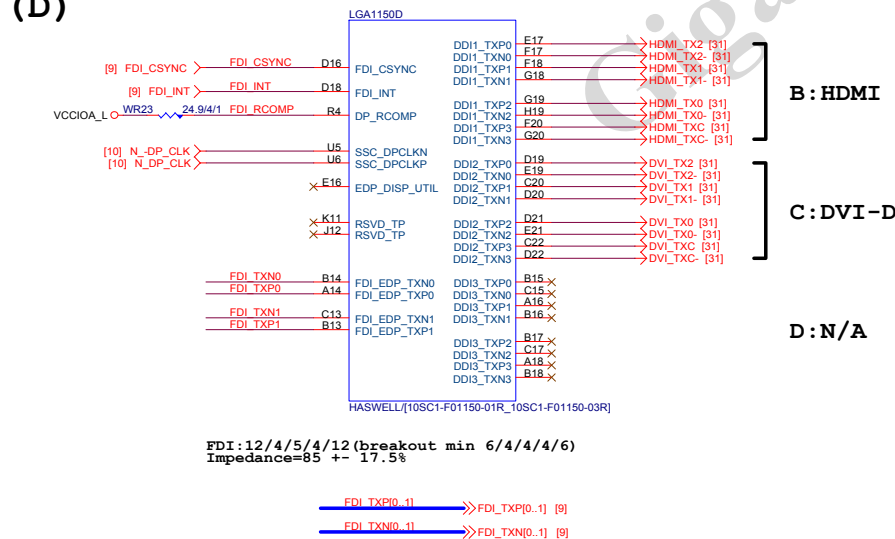
LGA1150

(E)



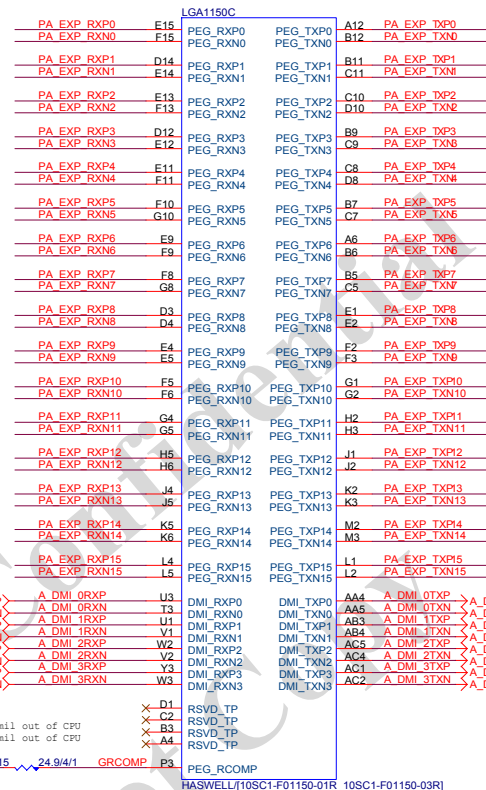
LGA1150

(D)

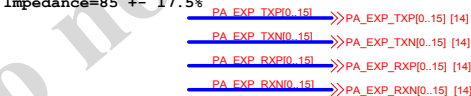


LGA1155

(C)

PCIEX16:16/5/5/5/16 (breakout min 10/4/4/4/10)
Impedance=80 +- 17.5%

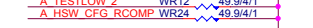
-CPURST

DMI:12/4/4/4/12 (breakout min 8/4/4/4/8)
Impedance=85 +- 17.5%

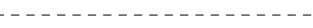
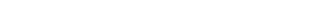
CPU SVID

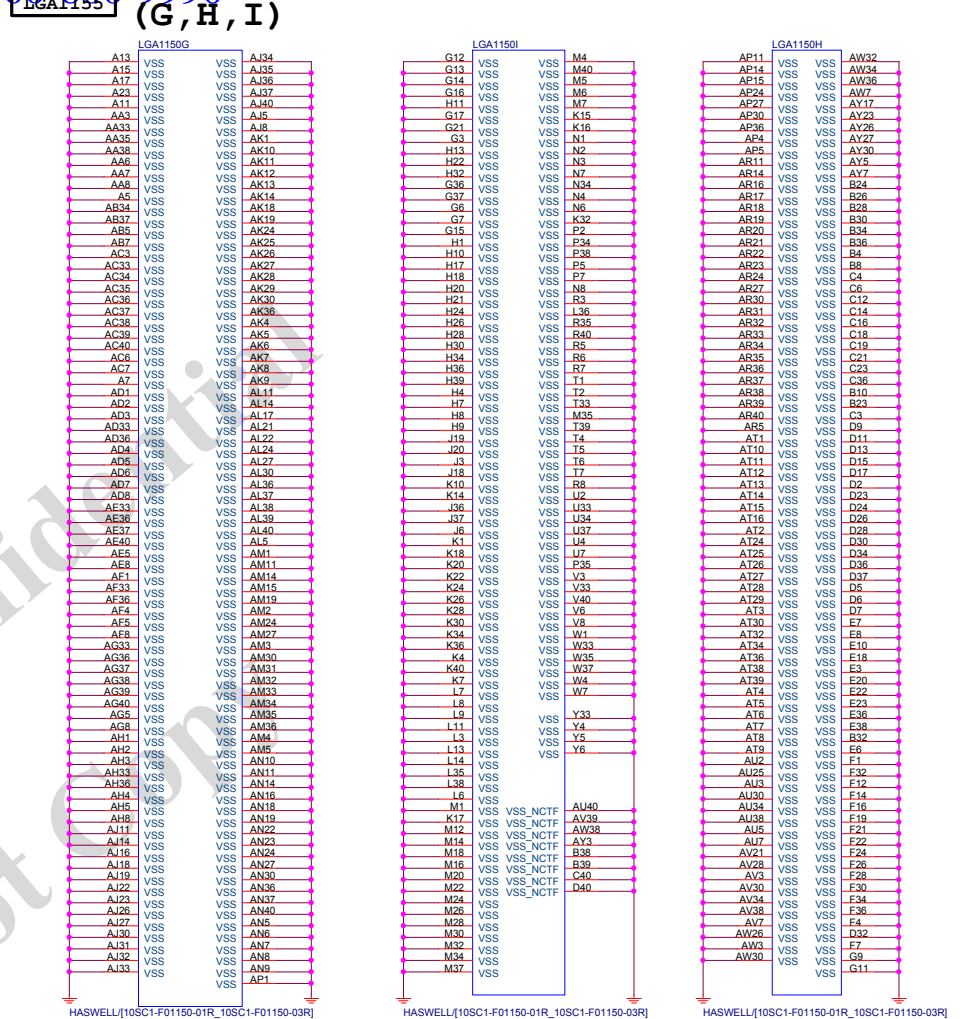


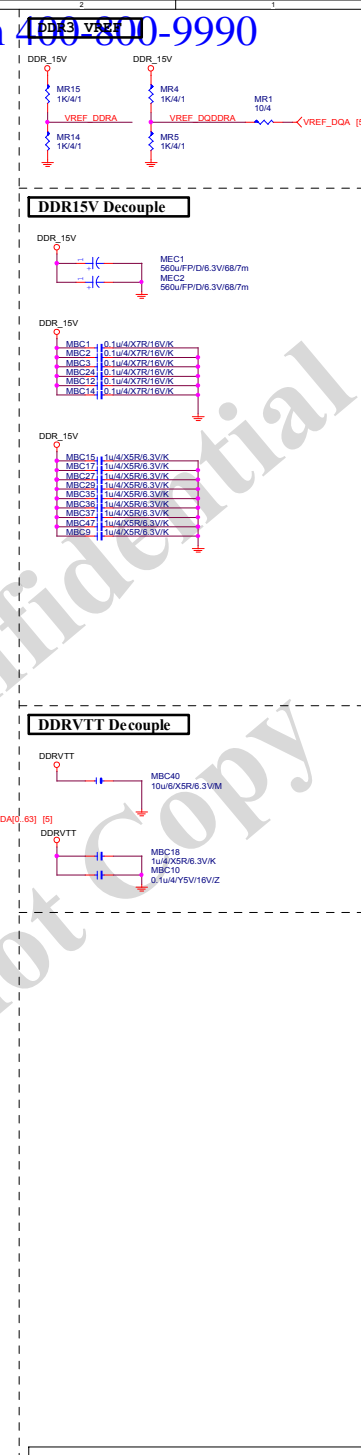
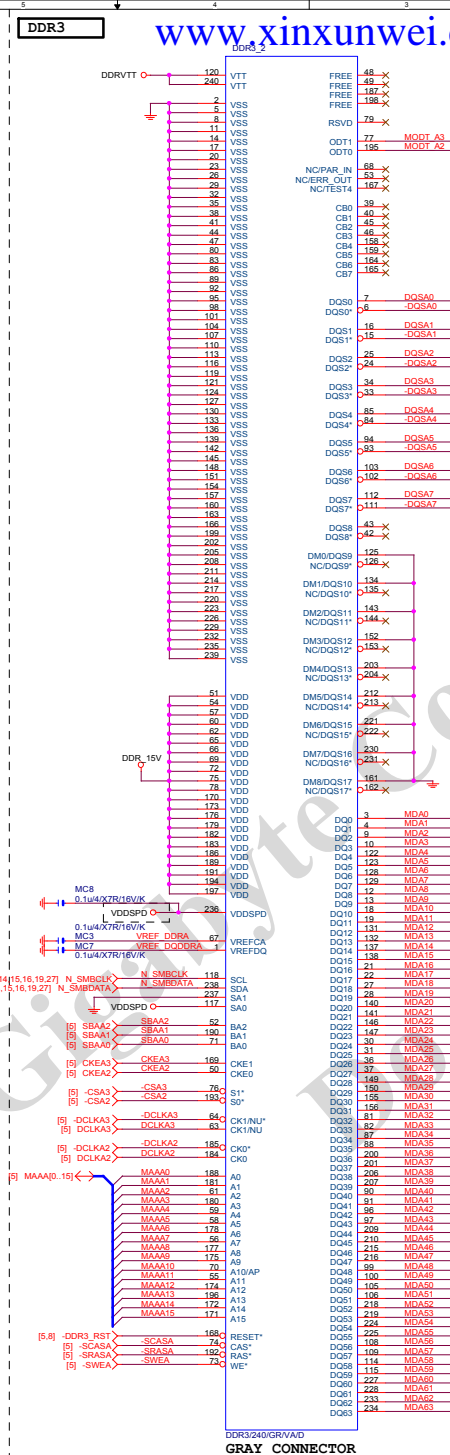
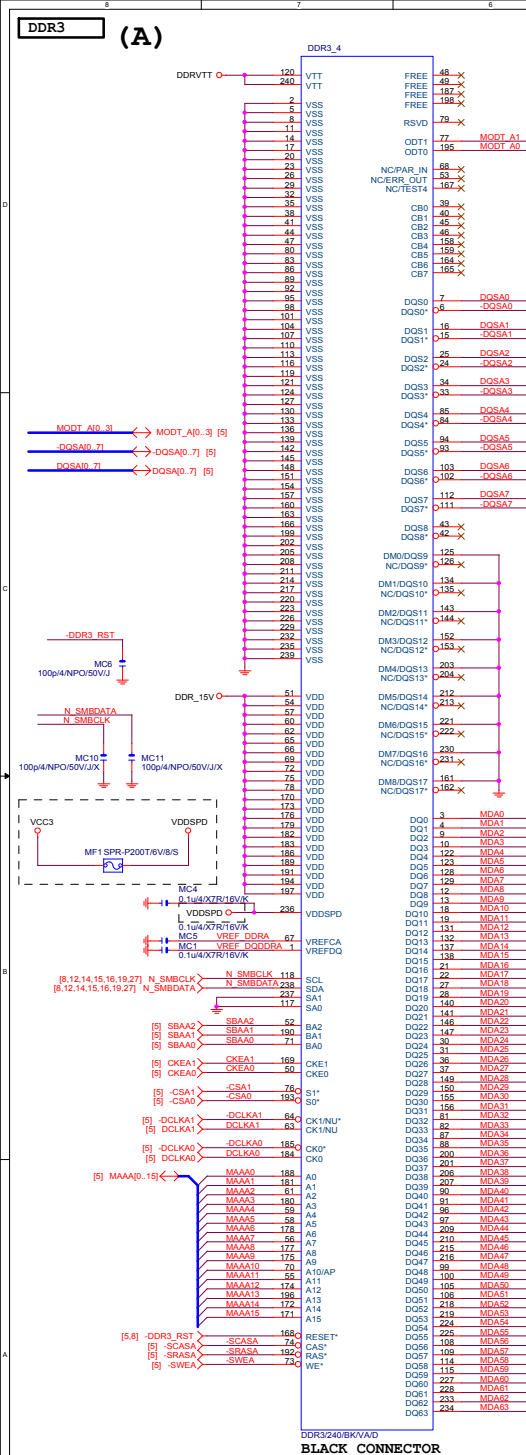
CPU PU/PD



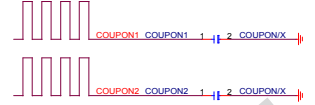
SM REF







VREF_DQDB [5]



CPU

DIMM4 (黒)

DIMM2 (灰)

DIMM3 (黒)

DIMM1 (灰)

CHA

CHB

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Title			
DDRIII CHANNEL B			
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PCH



PCHJ



SB HEATSIN



USB TABLE

USB OC# Configure	
OC0#	F_USB30
OC1#	R_USB30
OC2#	USB30_LAN
OC3#	F_USB3
OC4#	F_USB2
OC5#	KB_MS_USB
OC6#	F_USB1
OC7#	Not Use

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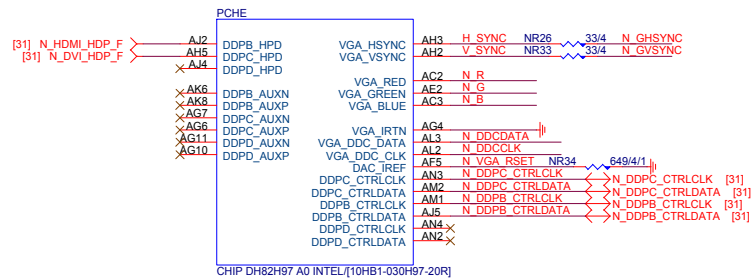
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Custom						1.0	
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PCH

(E)

PCH

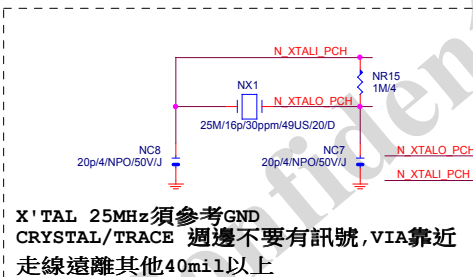
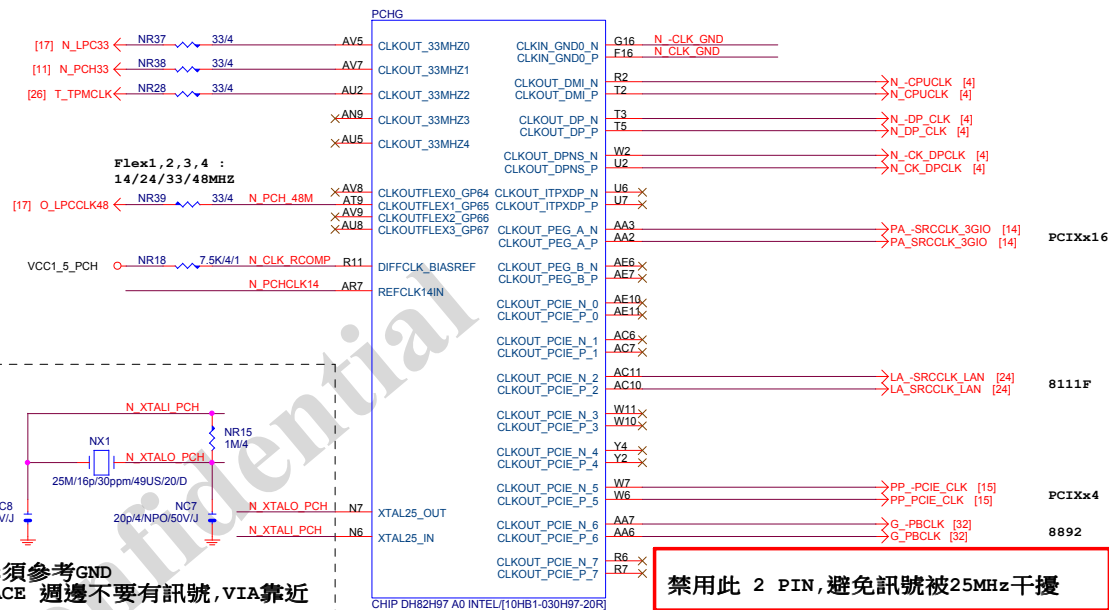
(G)



VGA DISABLE

R, G, B NC OR GND

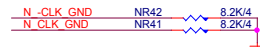
IRTN / IREF GND

VGA_HSYNC, VGA_VSYNC, DDC_CLK,
DDC_DATA NCPOWER VCCADAC (AF2),
VCCADACBG (AE1) GND

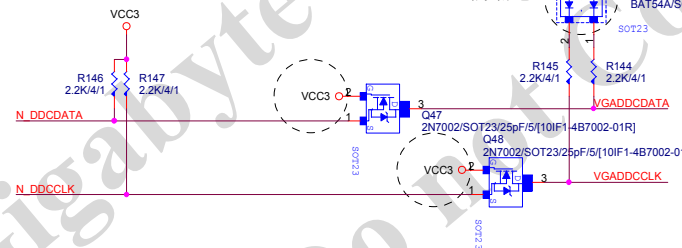
禁用此 2 PIN, 避免訊號被 25MHz 干擾

Differential Clock: 18/4/6/4/18
Impedance=90 +- 15%

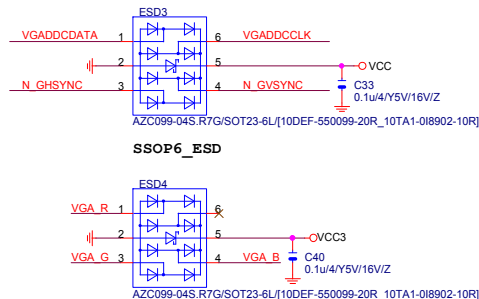
PCH CLK PD

Mount for integrated clock Generation
Mode

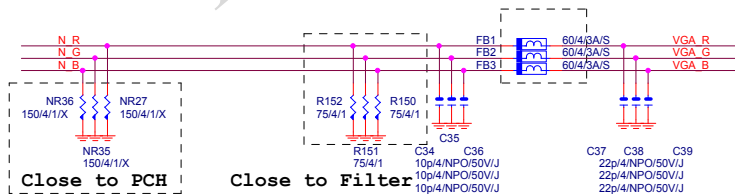
VGA DDC



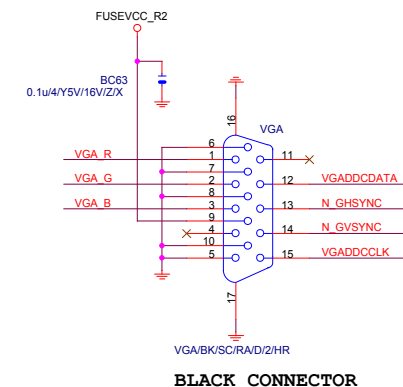
VGA ESD



VGA DDC



VGA CONNECTOR



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Title			PCH DISPLAY, CLK BUFFER		
Size			GA-H97M-D3H		
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(C)

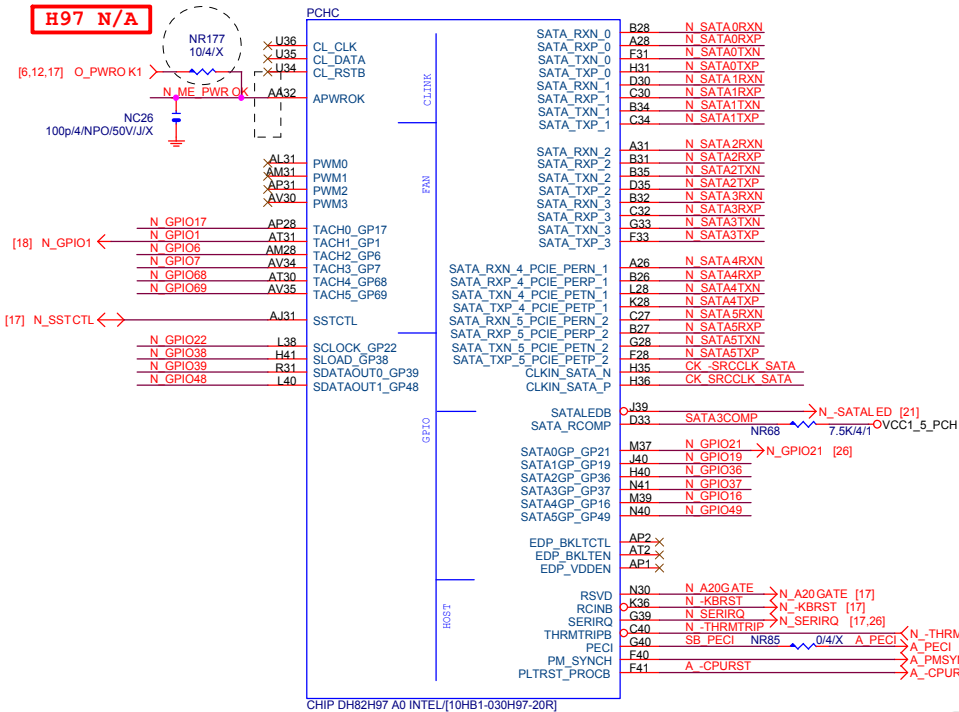
SATA3 : 20/7.5/4.5/7.5/20 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%
SATA2 : 15/7.5/4.5/7.5/15 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%

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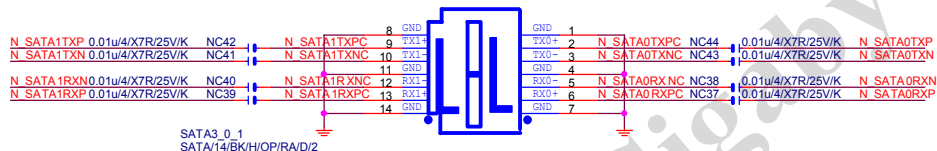
(A)

PCH CLK PD

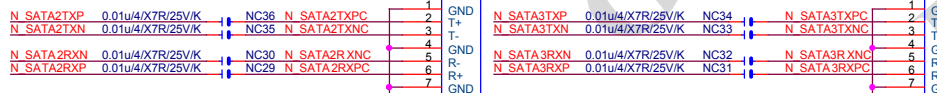
H97 N/A



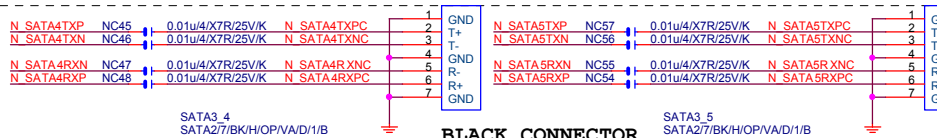
SATA CONNECTOR



H81 Port 2/3 N/A

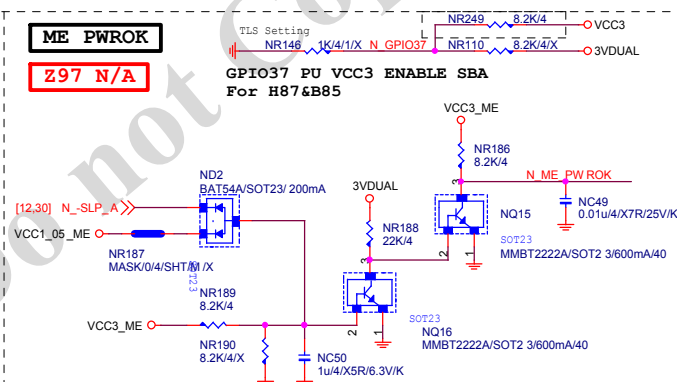


```
** Z87/H87 Port 4&5 SATA3.0
** B85 Port 4&5 SATA2.0
```

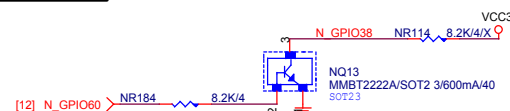


ME PWROK

Z97 N/A



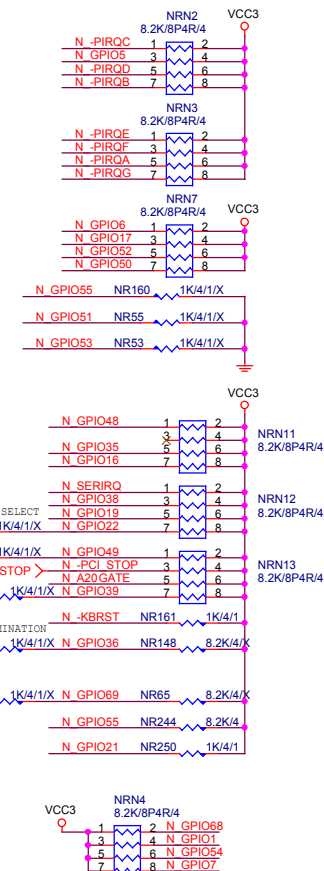
GPIO38 Ctrl



CK_SRCCLK SATA NR174 8.2K/4
CK -SRCCLK SATA NR173 8.2K/4

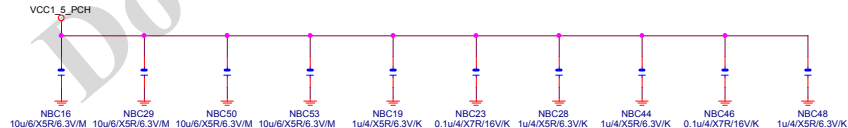
Mount for integrated clock Generation Mode

PCH	PU/PD
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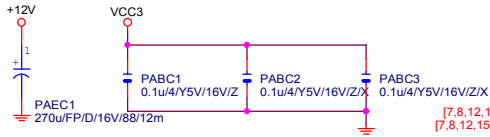


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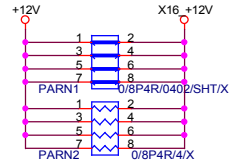
Title			
PCH HOST , SATA, PCI			
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PCIEX16 CAP



PCIEX16 PROTECT SHT



PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

PA EXP RXP0 [0..15] >>> PA_EXP_RXP[0..15] [4]

PA EXP RXN0 [0..15] >>> PA_EXP_RXN[0..15] [4]

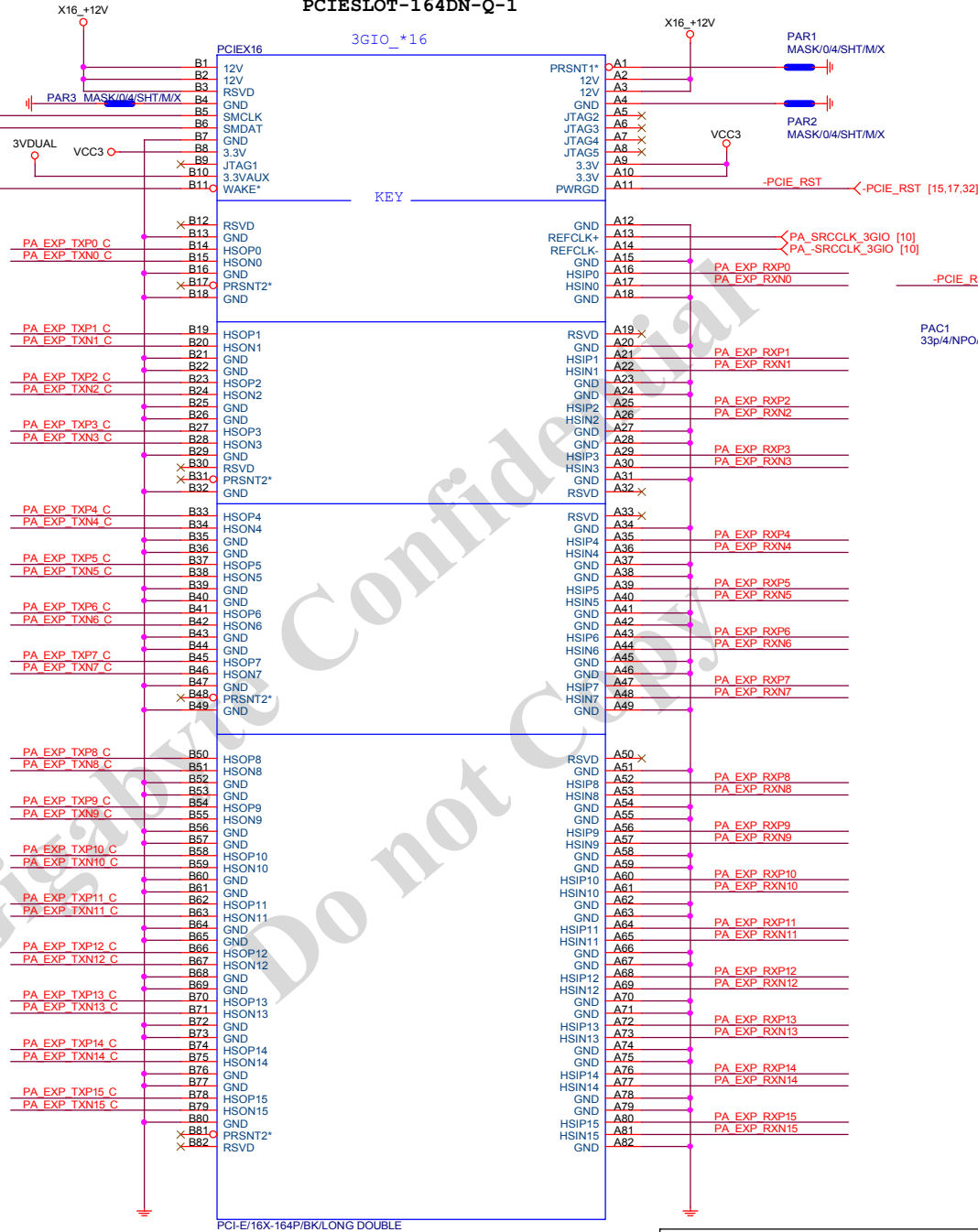
PA EXP TXP0 [0..15] >>> PA_EXP_TXP[0..15] [4]

PA EXP TXN0 [0..15] >>> PA_EXP_TXN[0..15] [4]

PCIEX16 SLOT

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PCIESLOT-164DN-Q-1



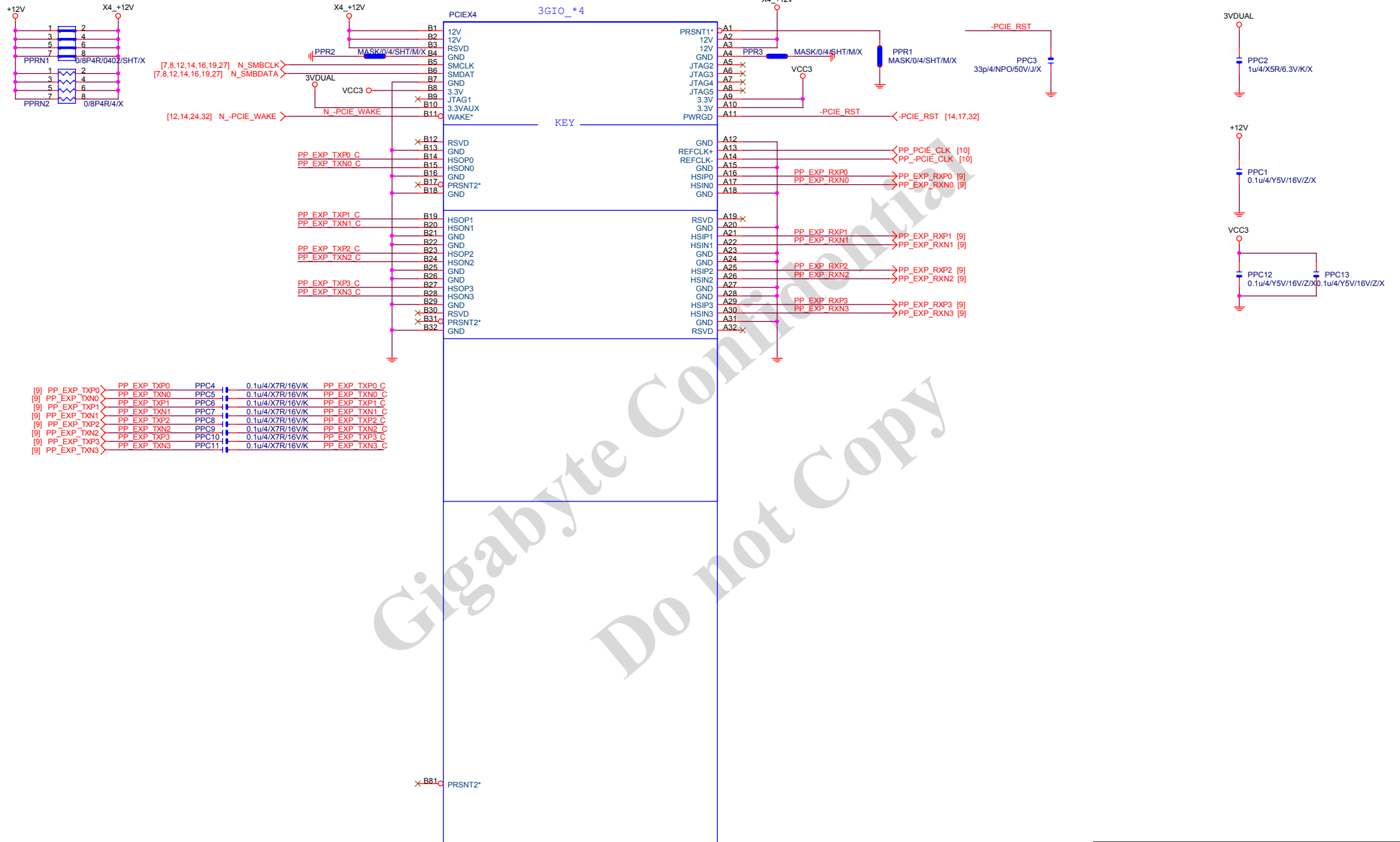
PCI-E/16X-164P/BK/LONG DOUBLE

BLACK CONNECTOR

Gigabyte Technology

Title			PCI EXPRESS * 16	
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PCIESLOT-64D-98D-P

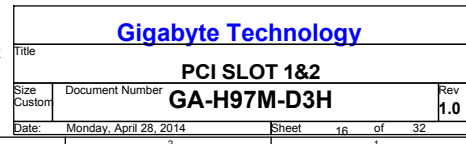
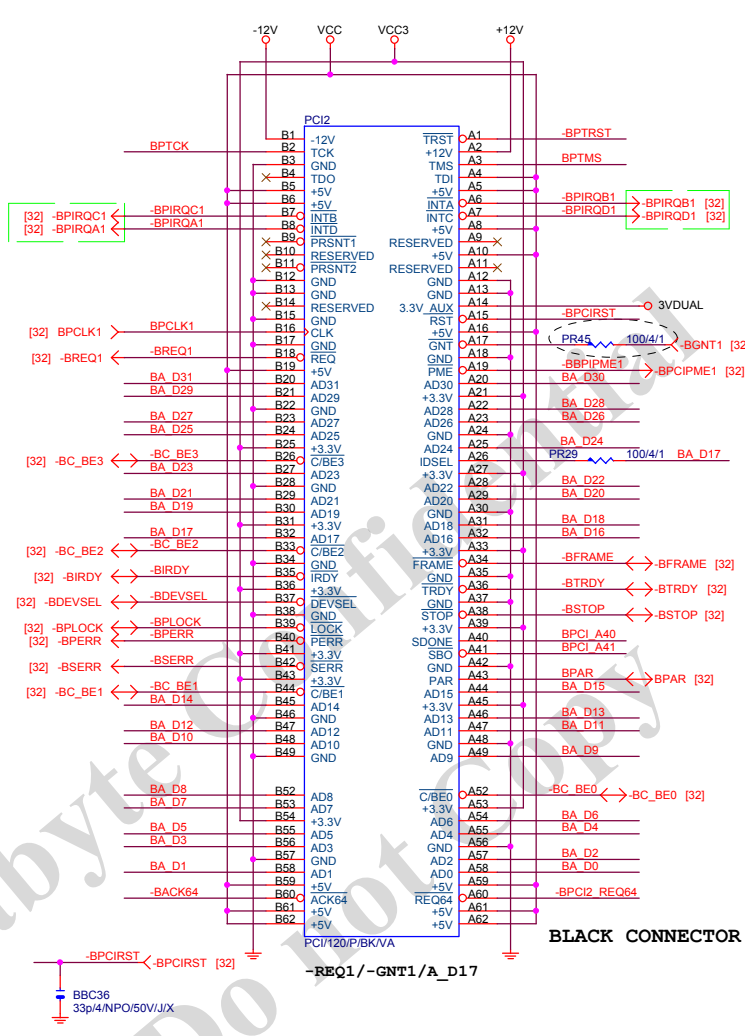


PCI-E/4X-65P/BK/LONG DOUBLE

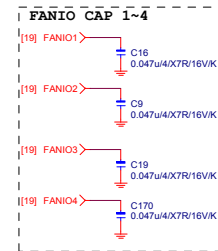
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Gigabyte Technology

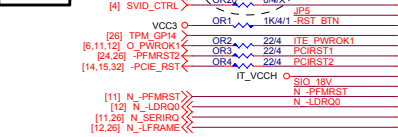
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			Rev 1.0		



SIO IT8620

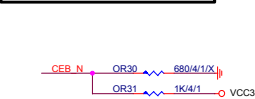


-PROCHOT

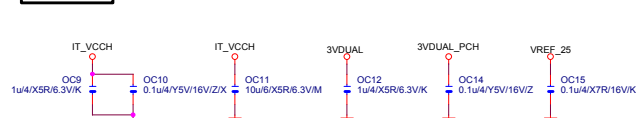


IT8620E GPIO問題調整	
PIN 50	第一次接上POWER時會拉 LO
PIN 90/91	DEFAULT為HOLD FUNCTION, GP93 BYPASS TO GP92
PIN 108	高溫時 GP92 會被拉 Lo (YSB 會被拉 Lo POWER ON 時會拉 Lo
PIN 111/112	MOUSE 與FANS FUNCTION 擇一使用, 不然會互相干擾

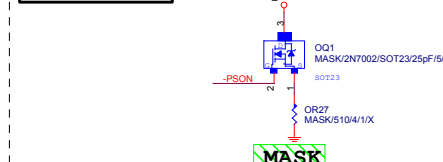
DUAL BIOS OPT STRAP



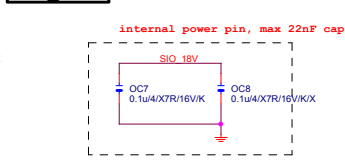
SIO CAP



Power leakage N/A



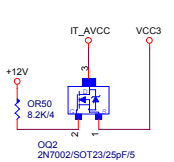
SIO 18V



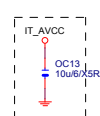
MB ID



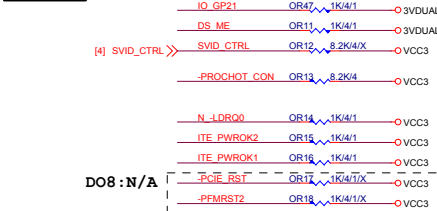
FIX MAX 插拔漏電



PWR SHT



SIO PU



DO8: N/A

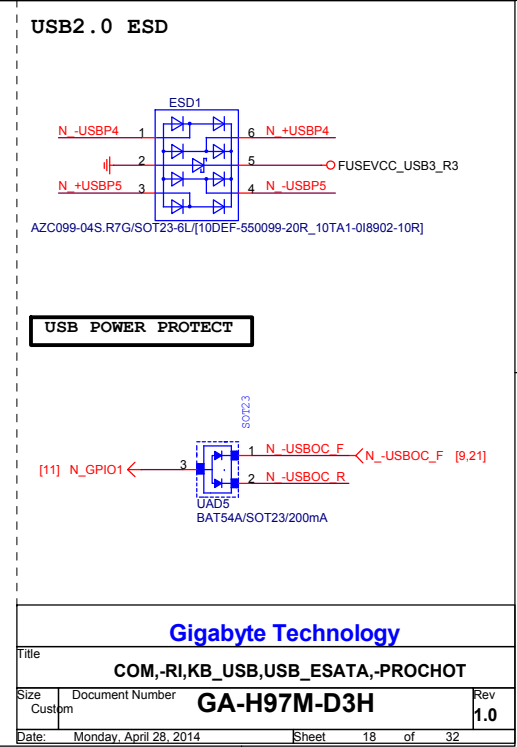
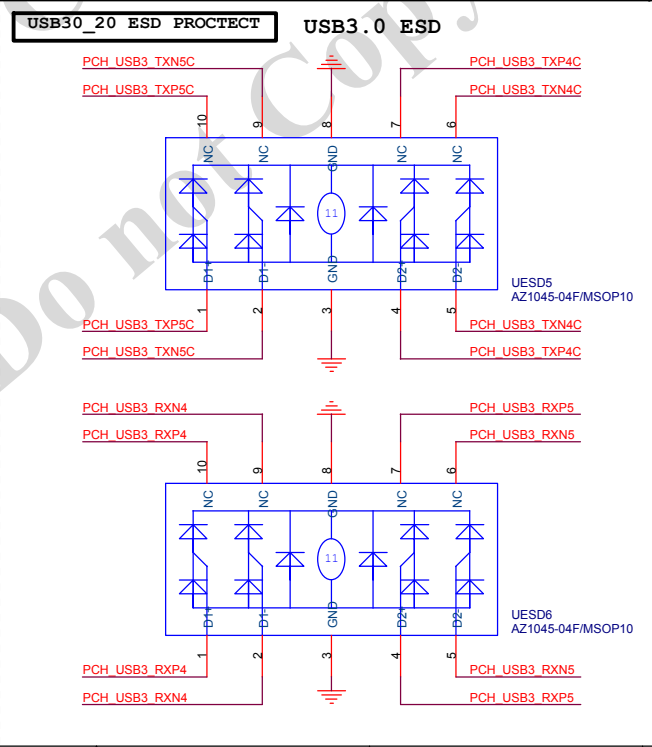
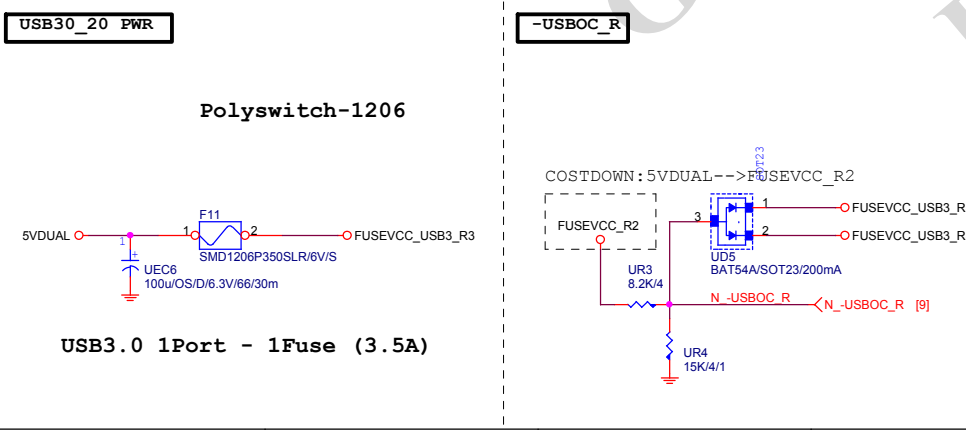
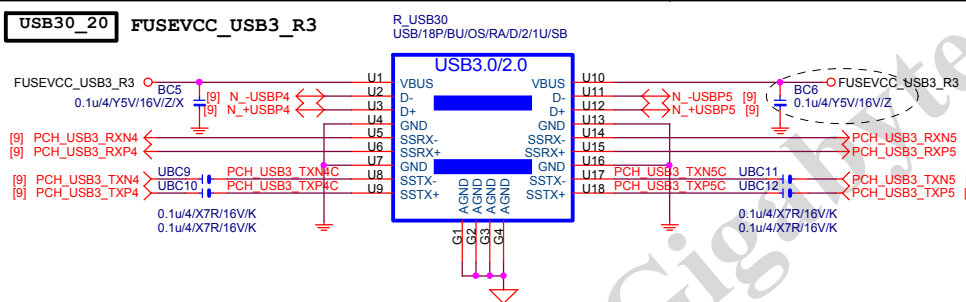
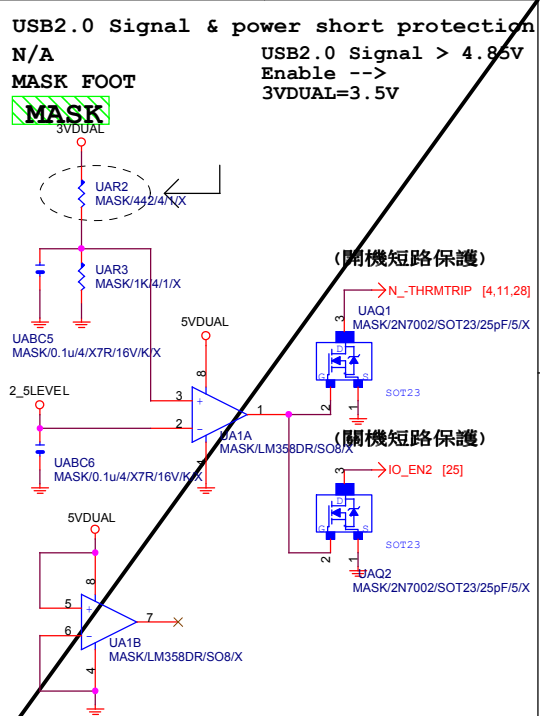
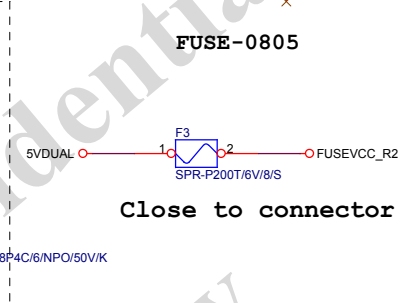
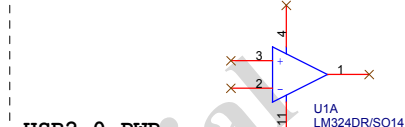
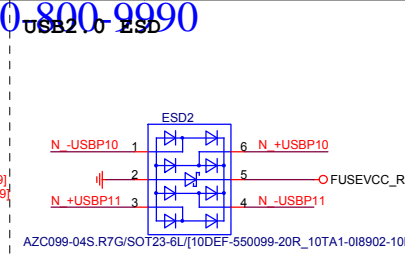
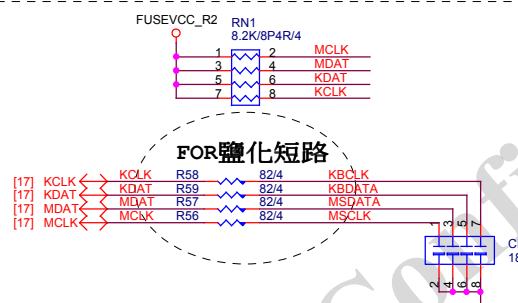
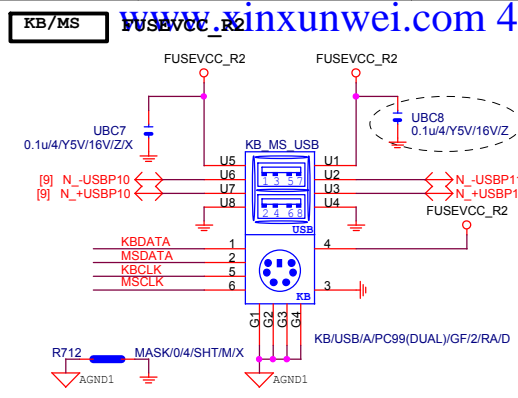
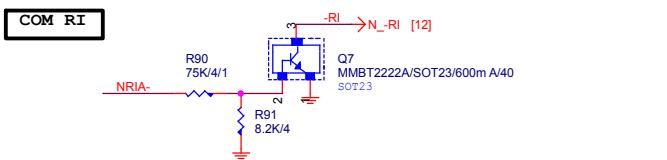
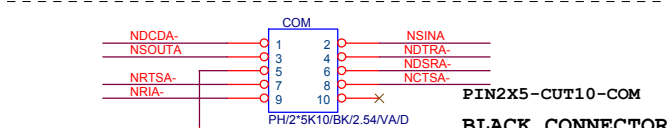
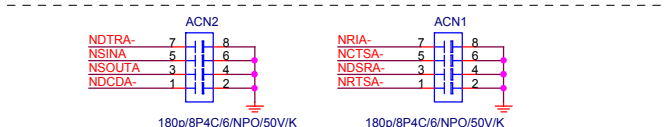
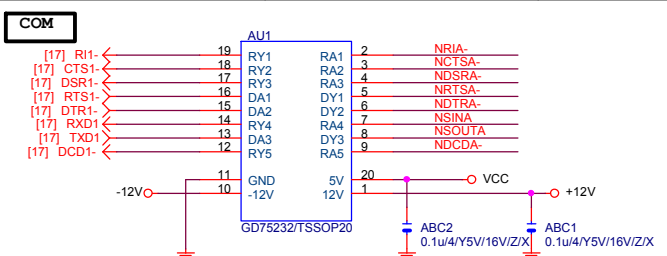
SIO STRAP



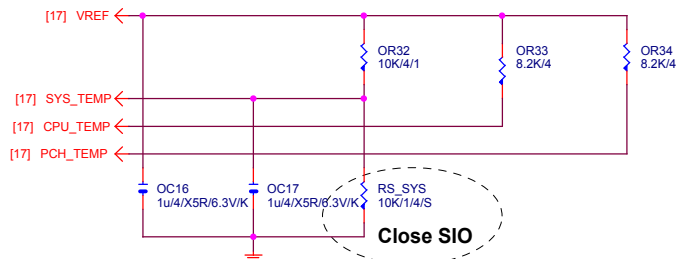
JP4	1	k8 power sequency function is Disable
JP4	0	k8 power sequency function is Enable
JP3	1	The default value of EC Index 63h/6Bh/73h is 80h.
JP3	0	The default value of EC Index 63h/6Bh/73h is FFh
JP5	1	The default value of EC Index 63h/6Bh/73h is 00h.
JP5	0	The default value of EC Index 63h/6Bh/73h is 40h.

Gigabyte Technology

Title	ITE 8728 LPC IO	
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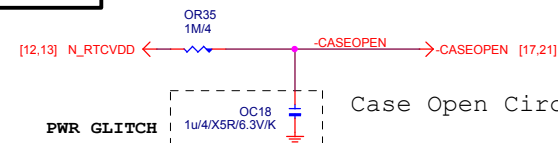


TEMP H/W MONITOR



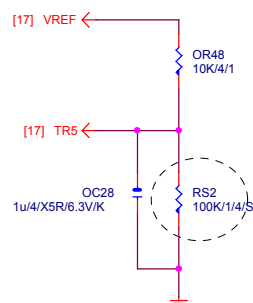
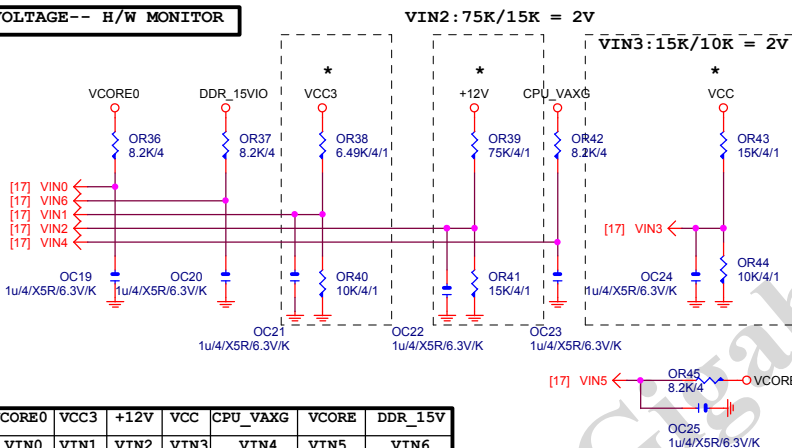
Close SIO

CASE OPEN



Case Open Circuits

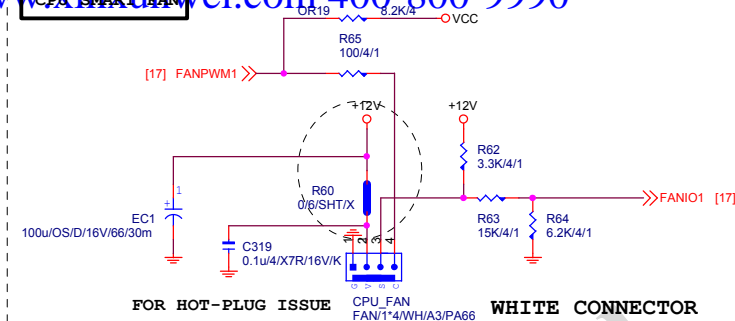
VOLTAGE-- H/W MONITOR



RS2 CLOSE CPU VR MOSFET
RS2 CLOSE MOSFET (VIN) : DCQ1

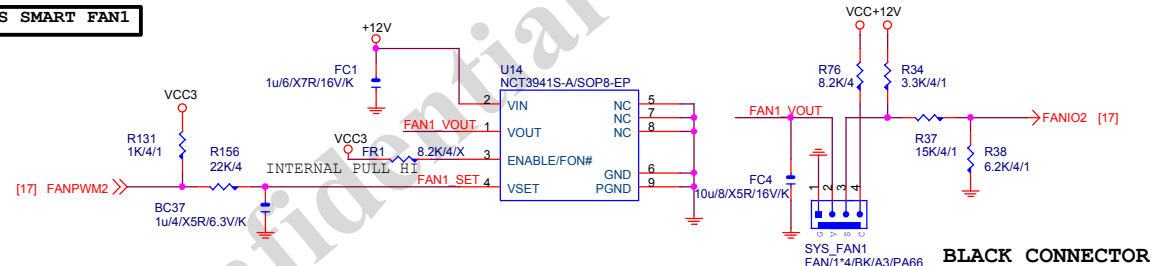
www.gigabyte.com 400-800-9990

CPU SMART FAN



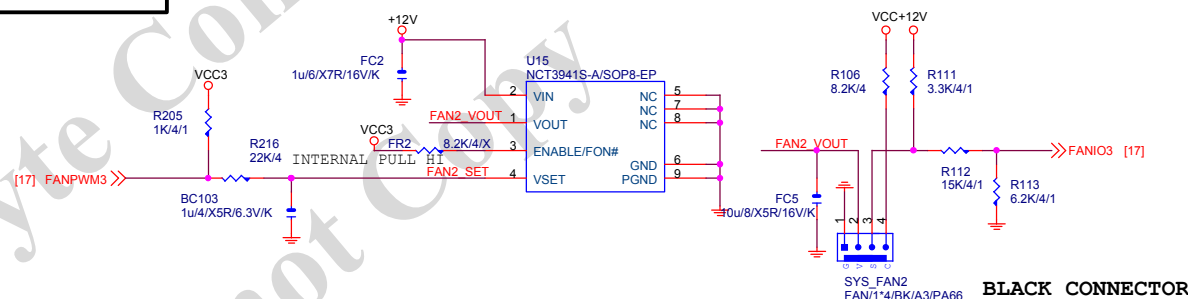
FOR HOT-PLUG ISSUE CPU_FAN FAN1*4/WH/A3/PA66 WHITE CONNECTOR

SYS SMART FAN1



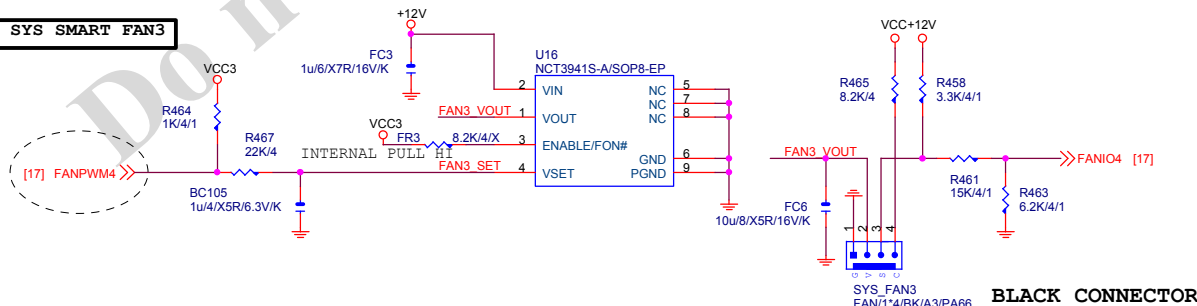
BLACK CONNECTOR

SYS SMART FAN2



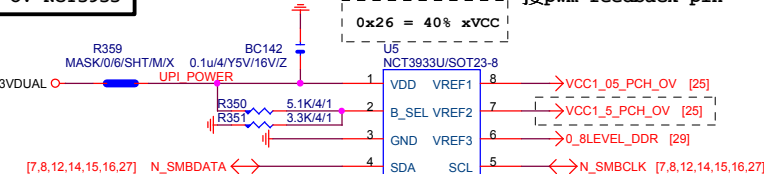
BLACK CONNECTOR

SYS SMART FAN3



BLACK CONNECTOR

OV NCT3933



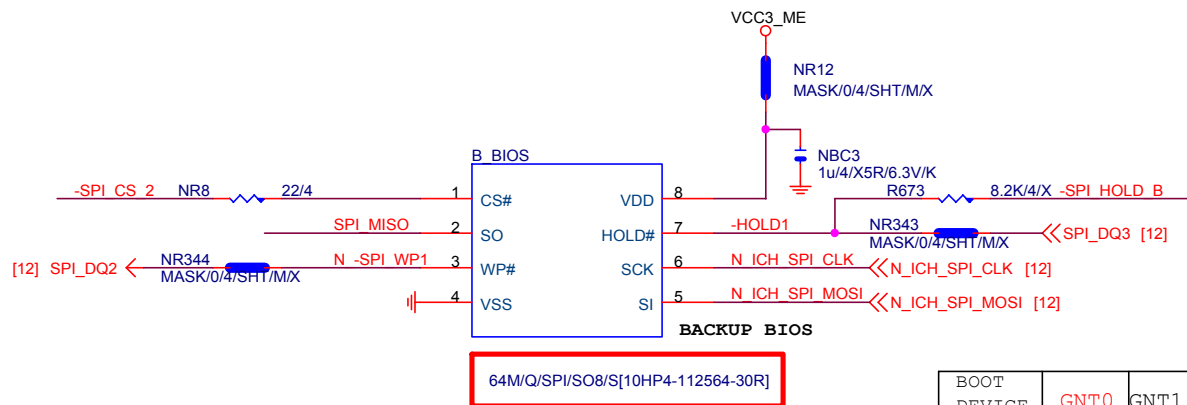
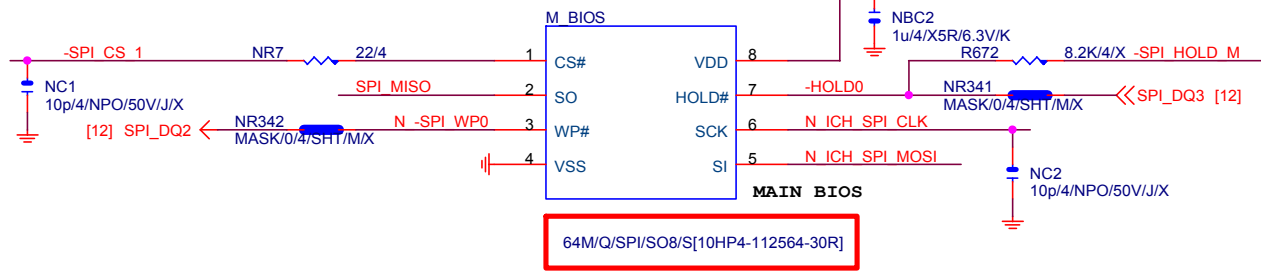
接pwm feedback pin

Gigabyte Technology

Gigabyte Technology				
Title				
HWM,FAN CTRL,OV				
Size	Document Number			Rev
Custom	GA-H97M-D3H			1.0
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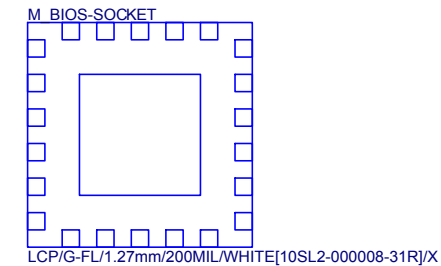
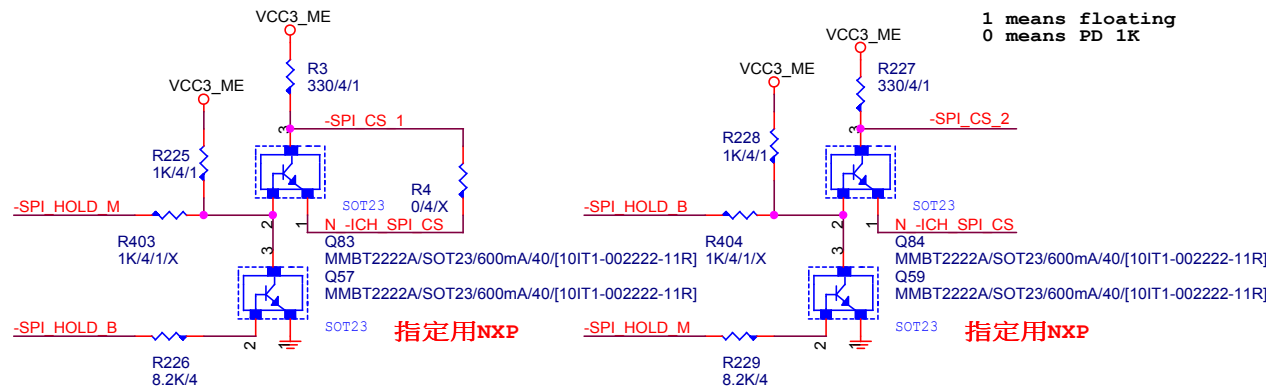
BIOS DEBUG PORT

BIOS_PH R1.0 移除

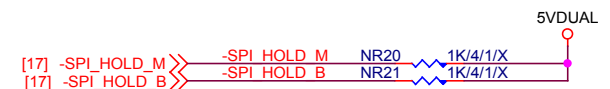
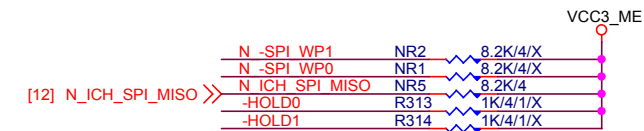
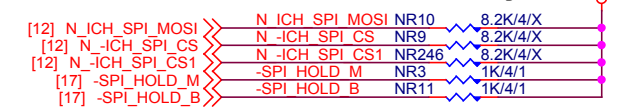


BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K



MOSI For DMI RX Termination Voltage

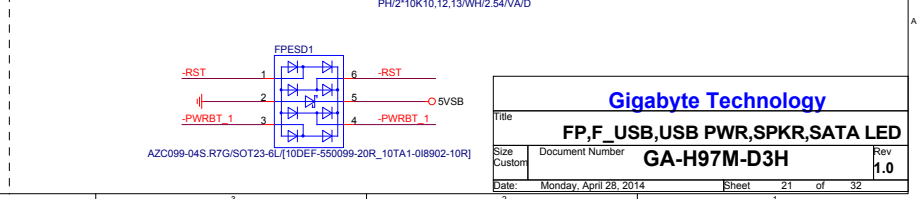
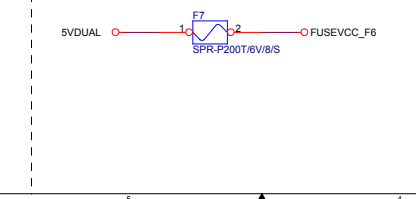
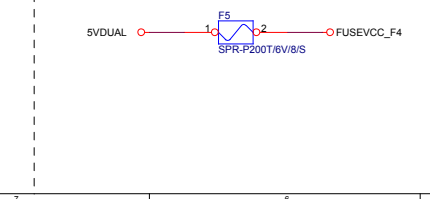
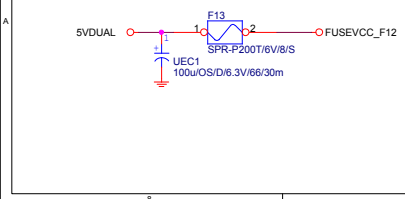
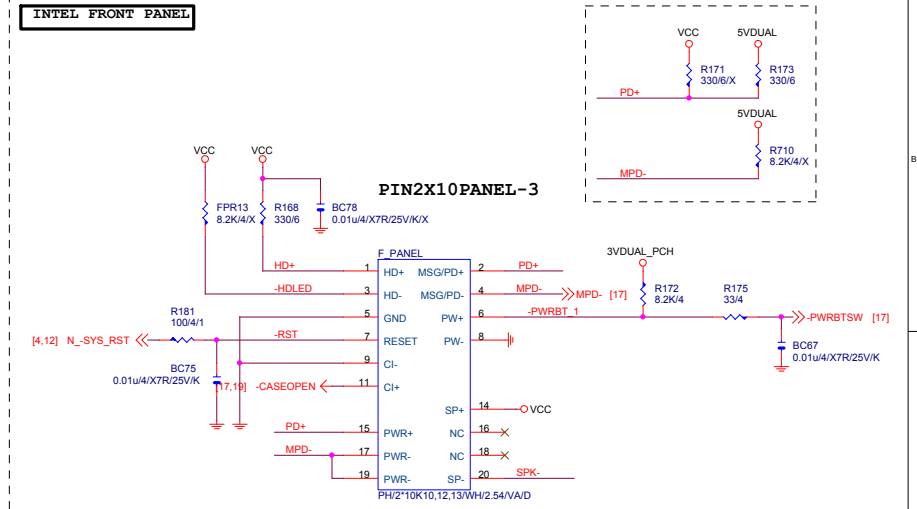
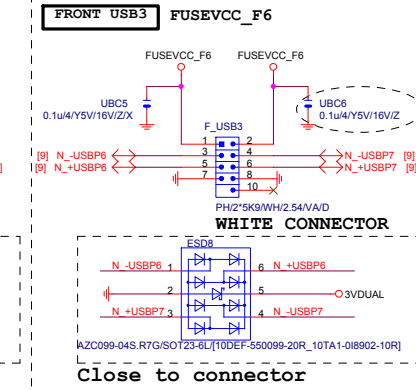
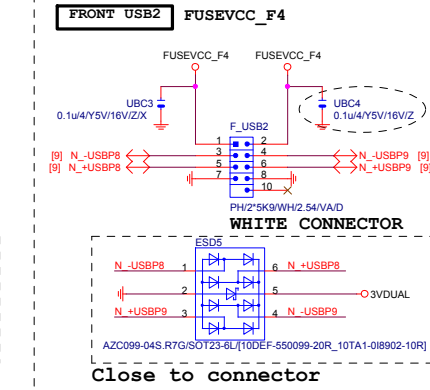
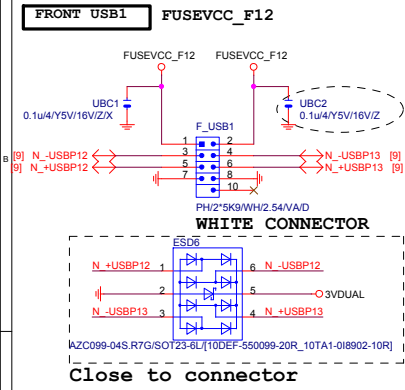
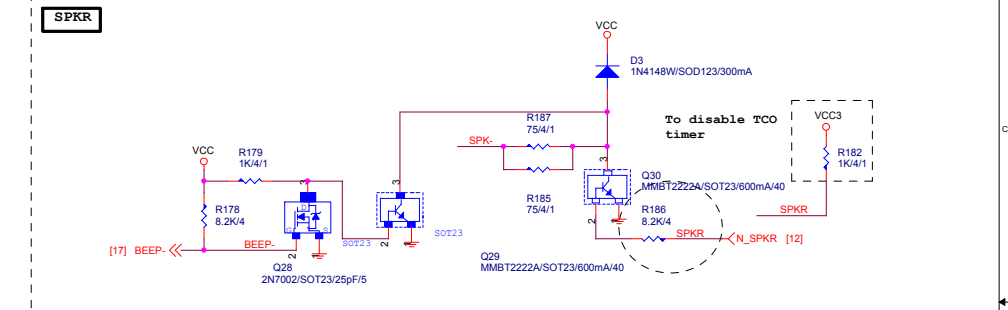
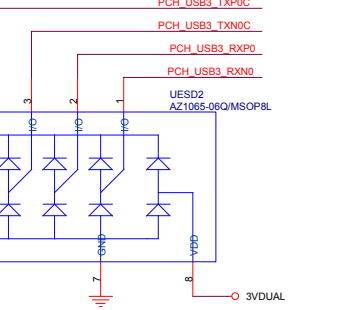
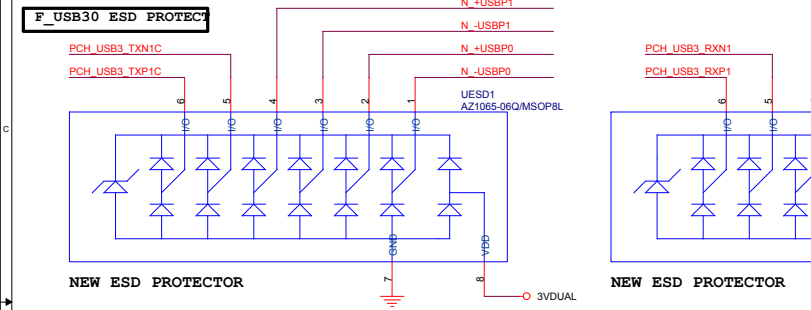
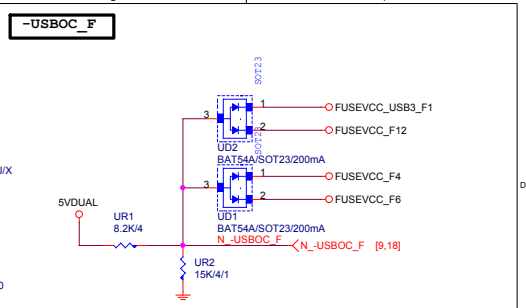
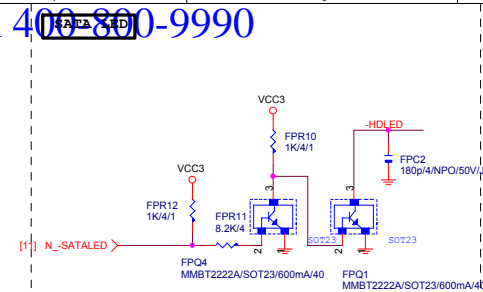
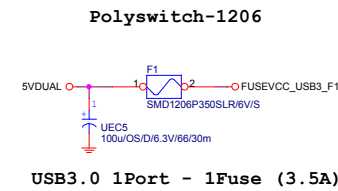
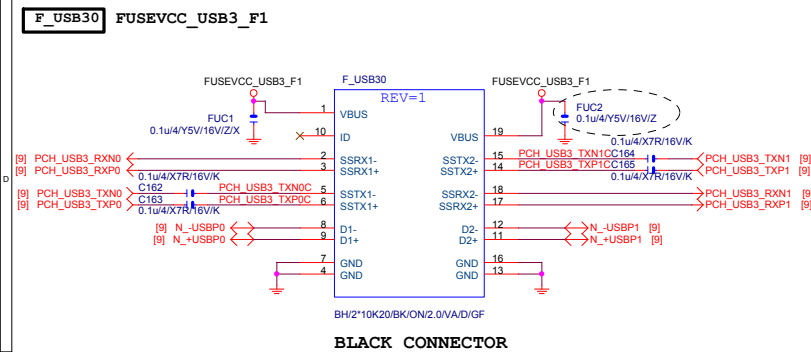


Gigabyte Technology

DUAL BIOS

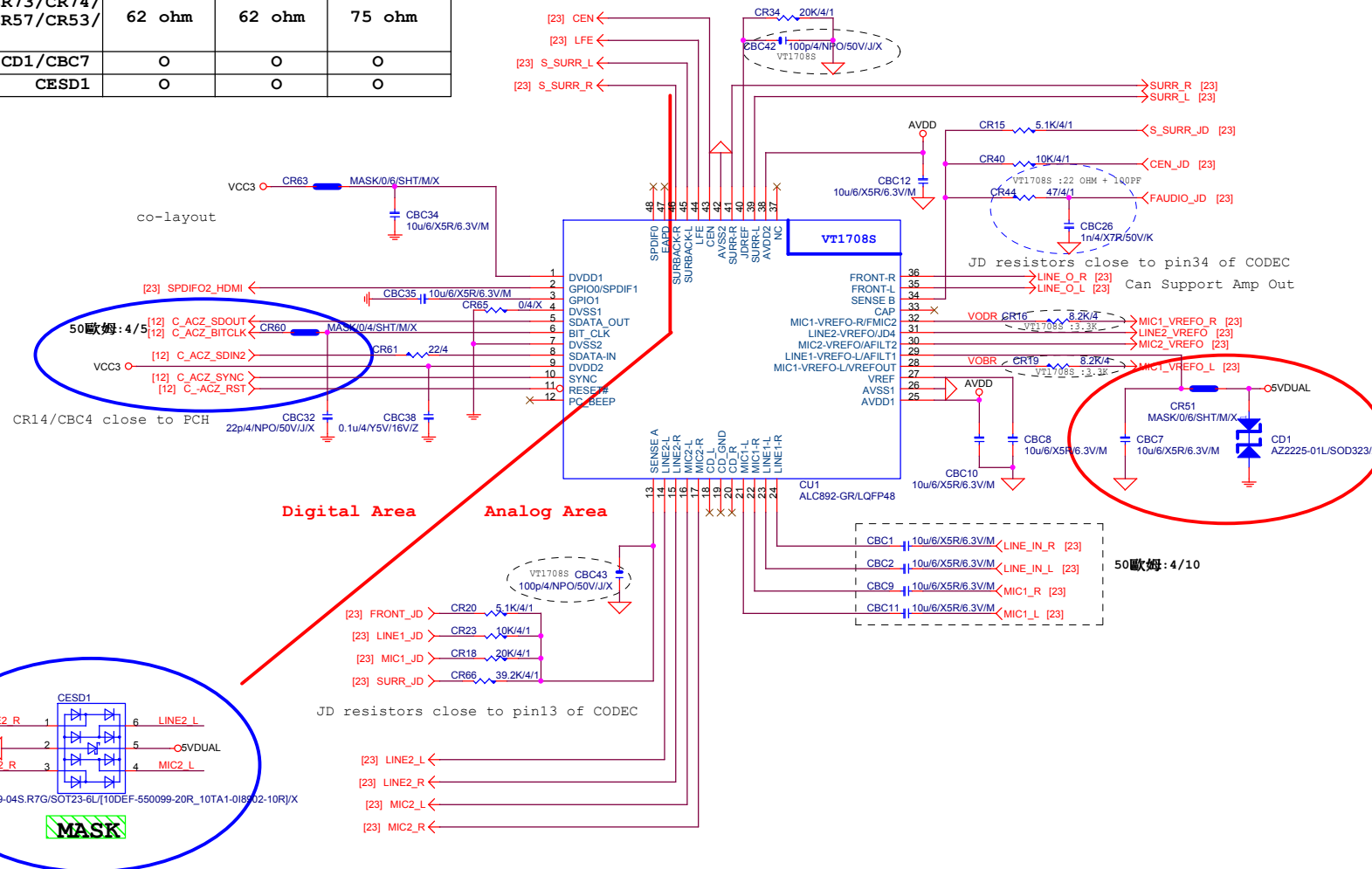
GA-H97M-D3H

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AZALIA CODEC ALC892/ALC887-VD2/VT1708-CE Colay

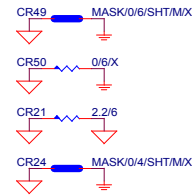
	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O



Gigabyte Technology

Title	HD AUDIO ALC887B-VD2/VT1708S/VT2021		
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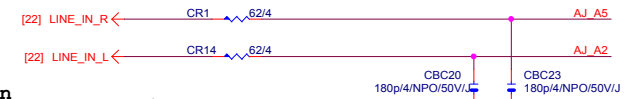
LINE-OUT



LINE-IN

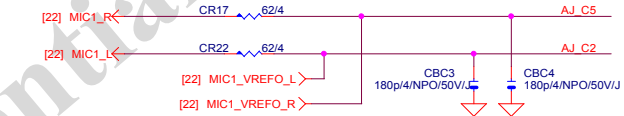
Verify MIC function
in LINE-in

Only reserved for ALC888

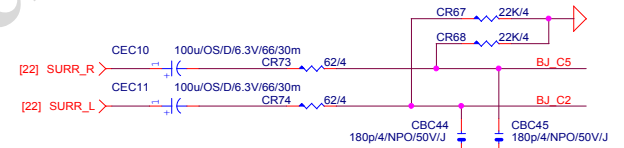


For 889A/888

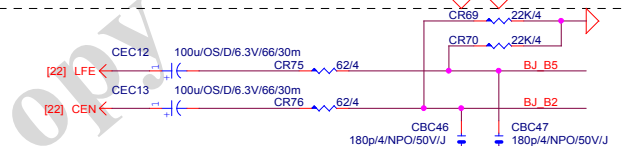
MIC-IN



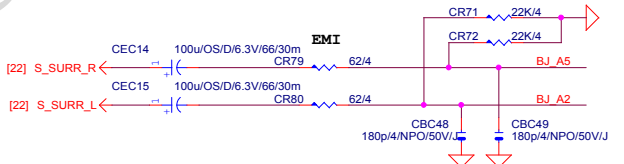
SURROUND



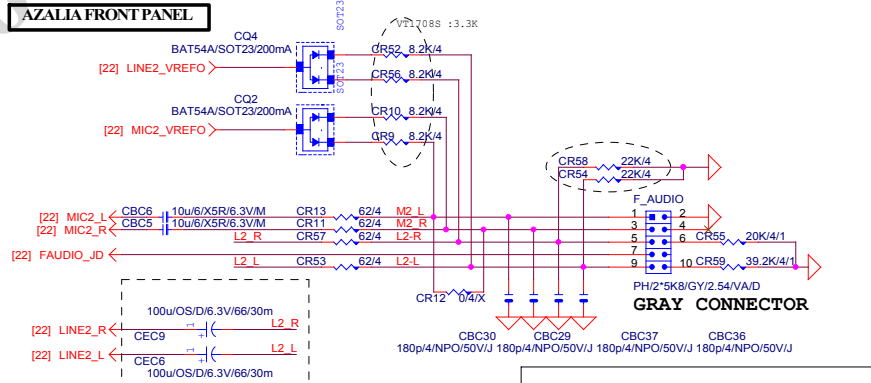
CEN/LFE



SURRBACK



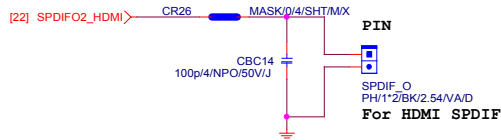
AZALIA FRONT PANEL



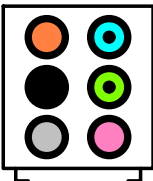
Gigabyte Technology

Title			
AUDIO JACK			
Size	Document Number	GA-H97M-D3H	
Custom			Rev 1.0
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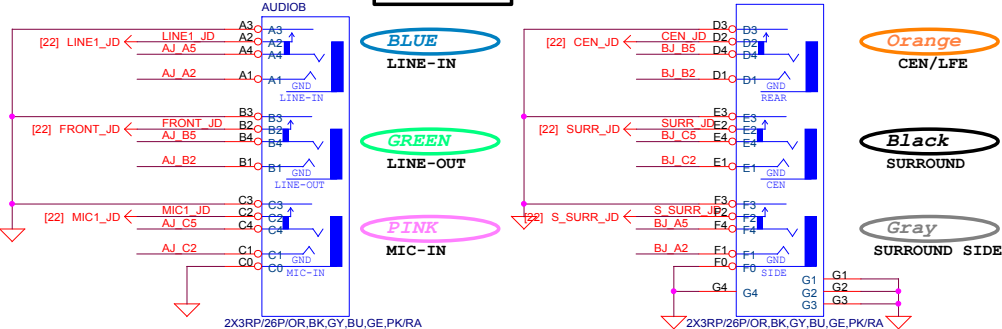
SPDIF_OUT

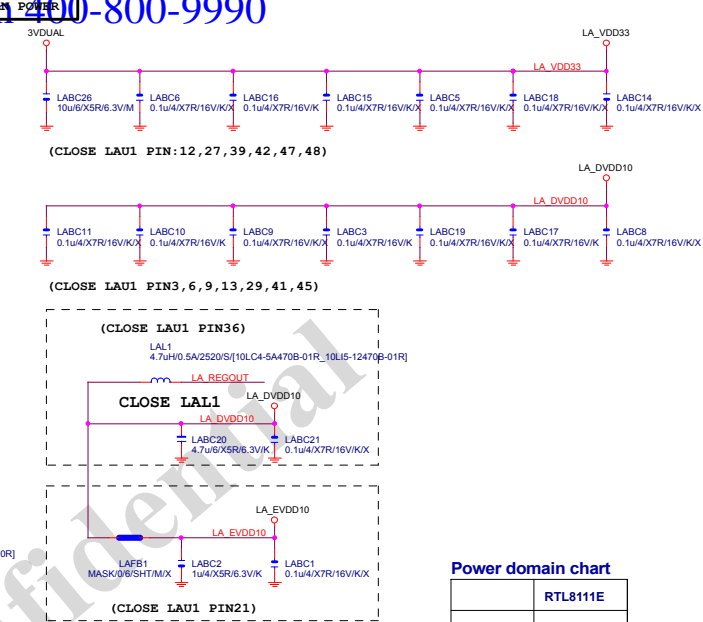
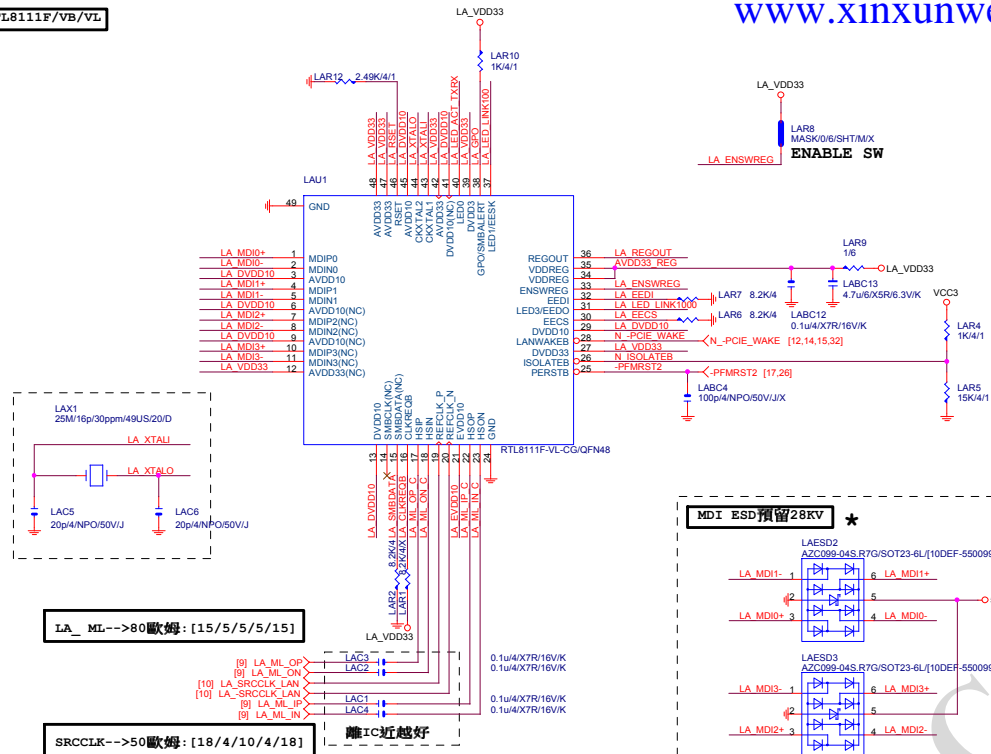


AZALIA JACK



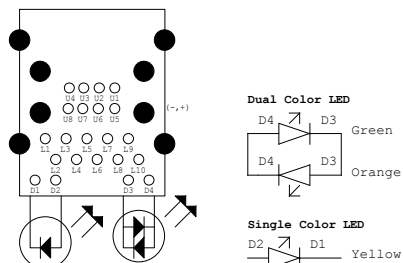
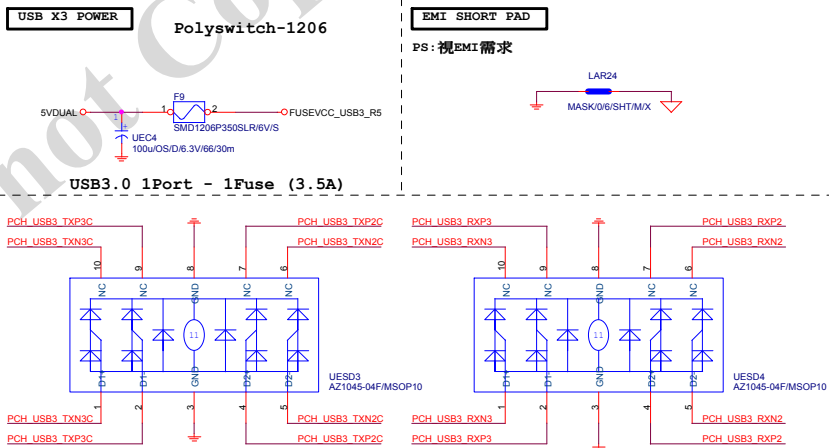
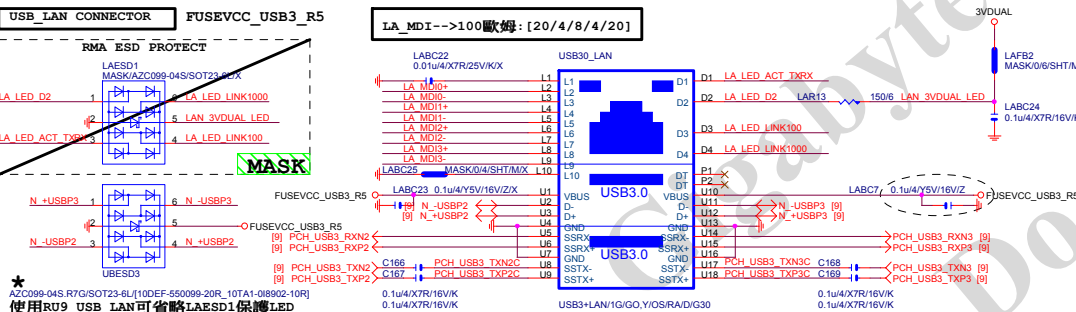
AZALIA JACK





Power domain chart

	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V



注意:USB PORT(目前:暫代6,7PORT)
USB-->90歐姆:[15/4.5/7.5/4.5/15]

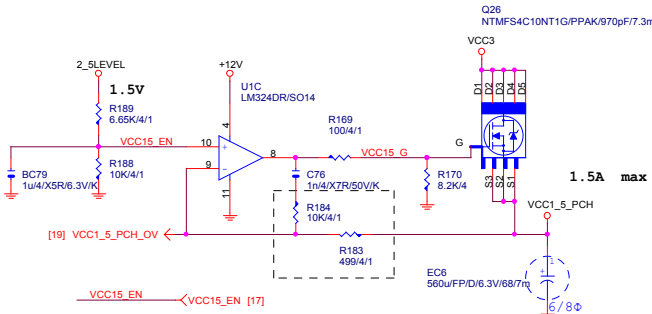
BOM NOTICE *

料號	規格	廠商
11NR6-702009-96R	1G LAN (12core)	UDE (RU9 ESD+)
[LED獨立走線,可省略外加AZC099料件LAESD1]		

1. 9KV ESD BOM:
USB_LAN (RU9):11NR6-702009-96R
2. 28KV ESD BOM:
USB_LAN (RU9):11NR6-702009-96R
LAESD2, LAESD3: 上件AZC398-04S

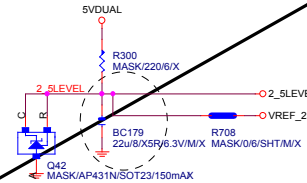
Gigabyte Technology			
Realtek RTL8111G			
Size Custom	Document Number	GA-H97M-D3H	Rev 1.0
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VCC1_5_PCH

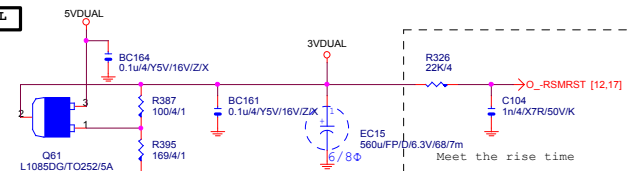


2_5LEVEL

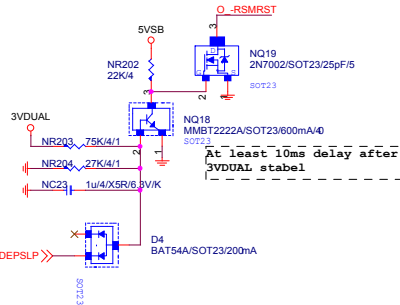
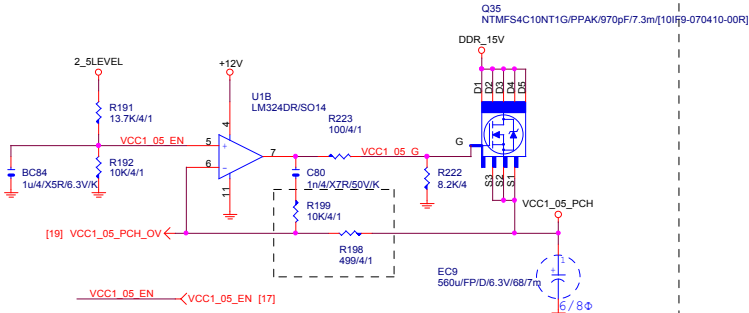
FOOT MASK
MASK



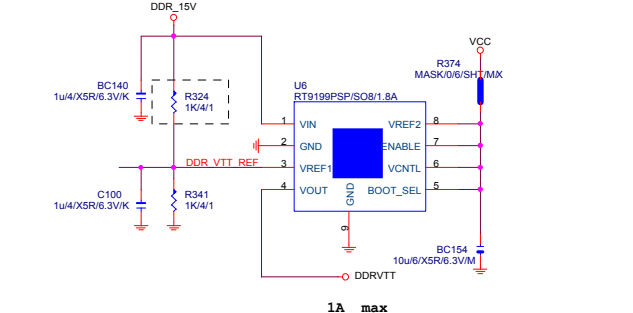
3VDUAL



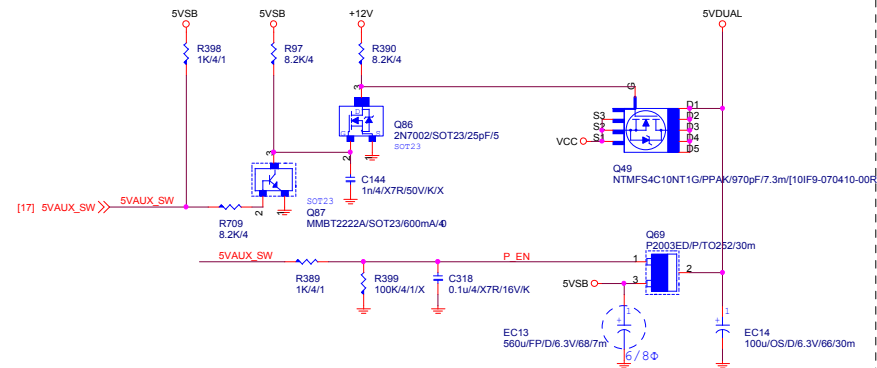
VCC1_05_PCH



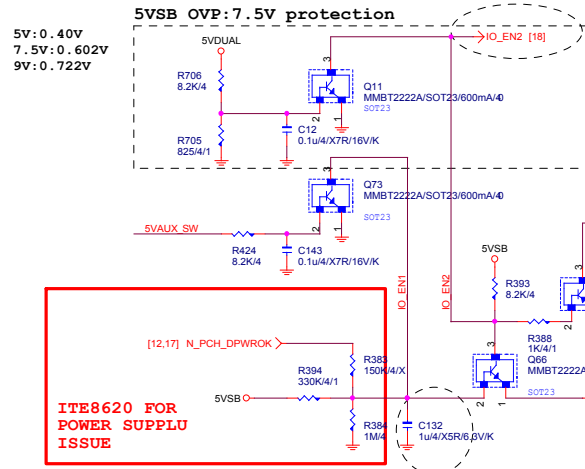
DDR_VTT



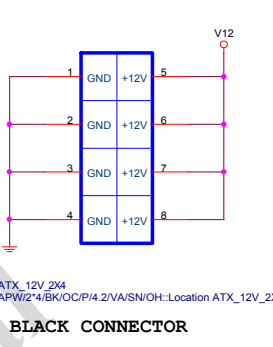
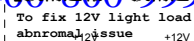
5VDUAL



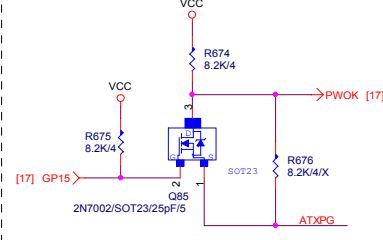
5VDUAL SHORT PROTECT

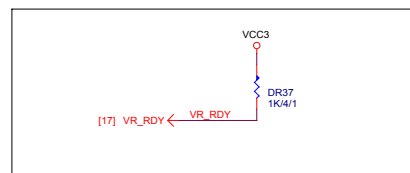
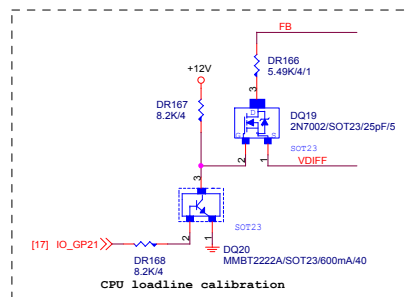


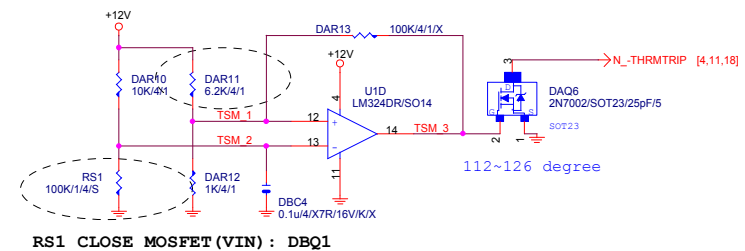
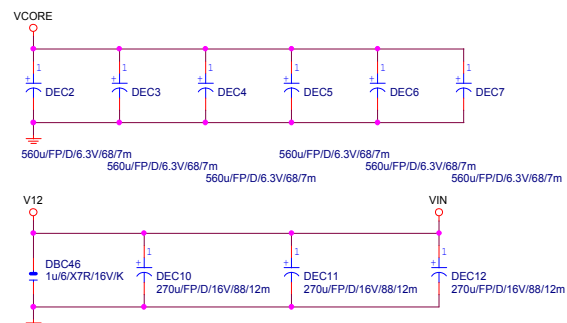
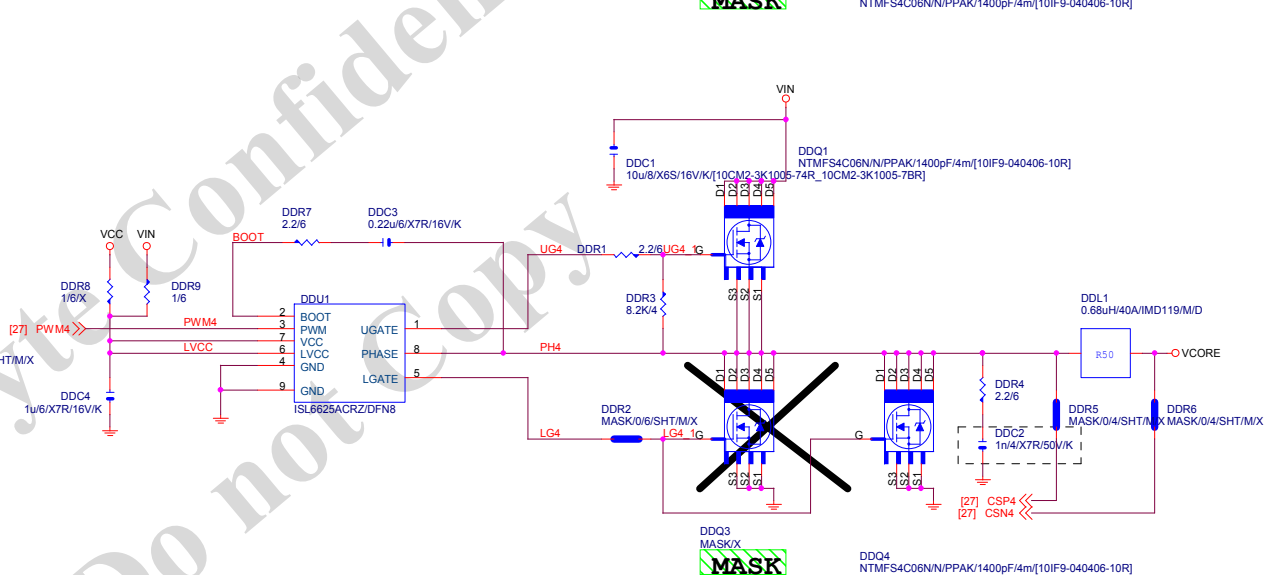
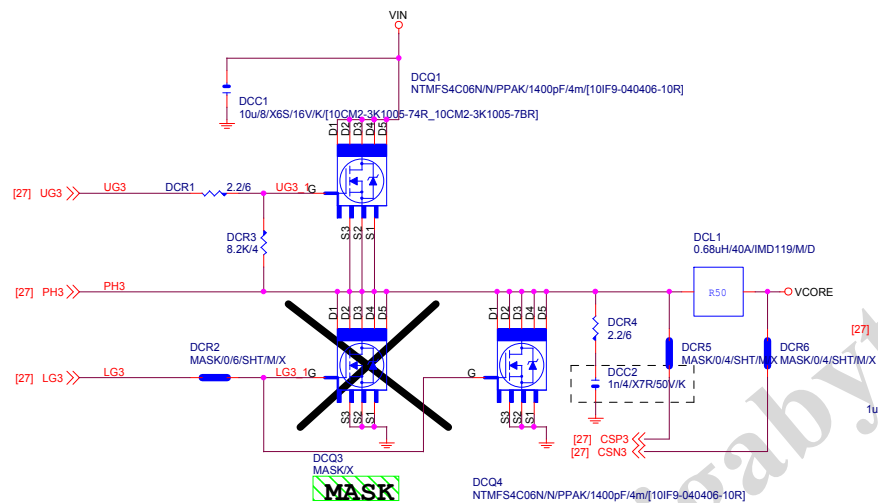
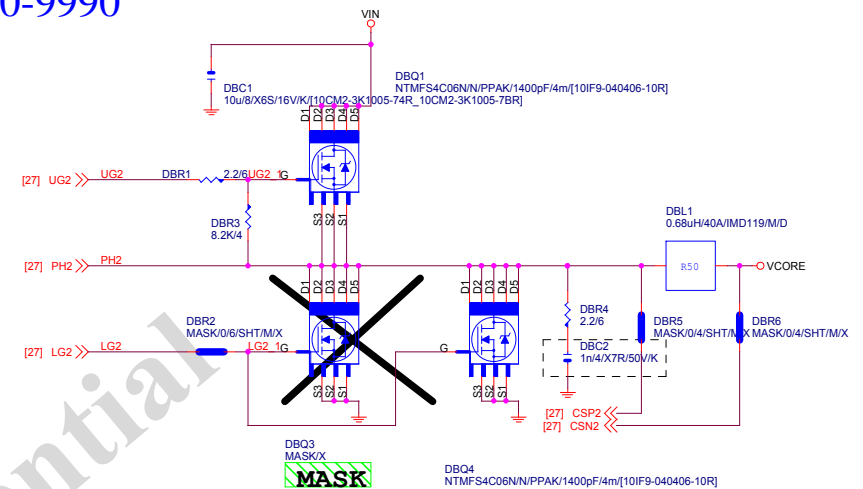
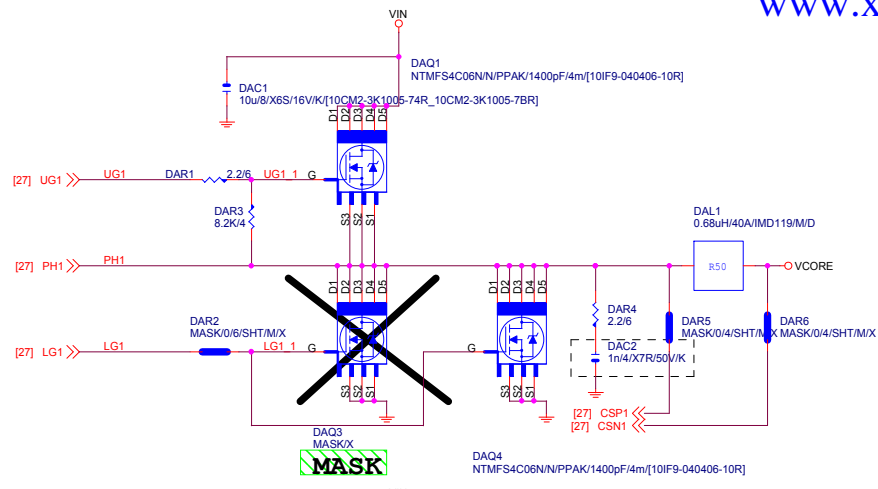
【技術通報R&D技術通報153】

[illegible]

【技術通報R&D技術通報154】





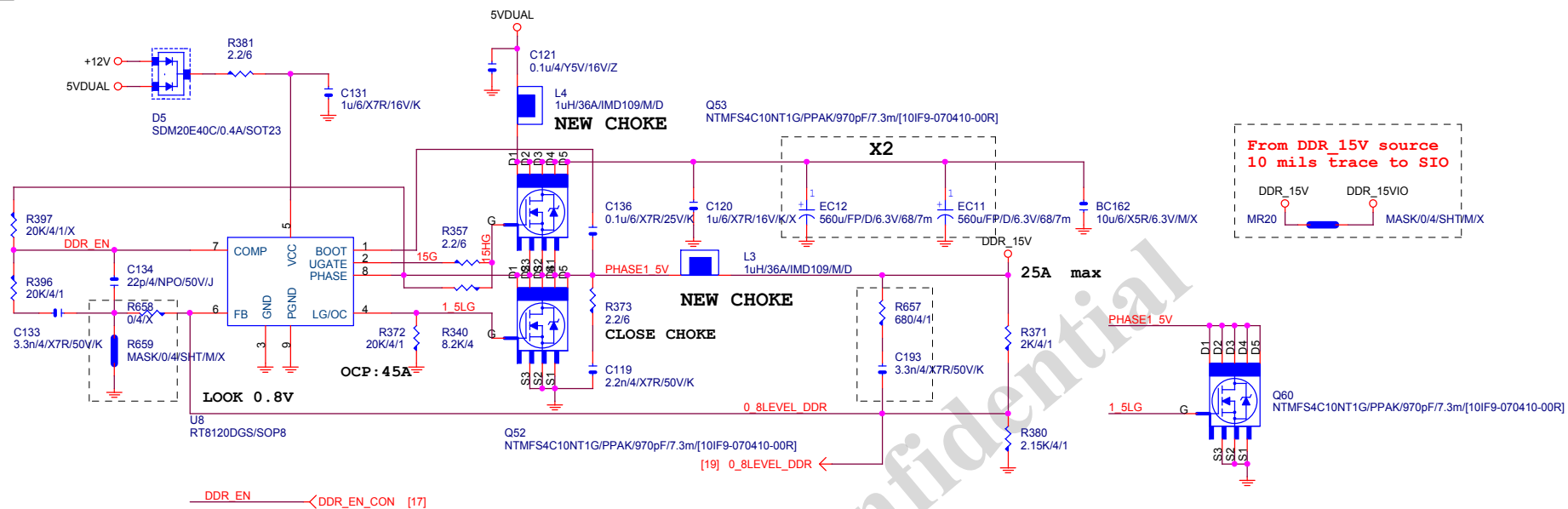


RS1 CLOSE MOSFET (VIN) : DBQ1

9 Series MOS Heatsink (Screw fix)

MOSHSINK-Z97X-SLI

H97 MODEL:一上一下,DAQ3,DBQ3,DCQ3,DDQ3改MASK FOOTPRINT



PWR SEQ

VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1
IRMS=11.45A
560uF/P/D/6.3V/68/8m RIPPLE CURRENT=4.7A
Coefficient=1.7(85°C), 1(105°C)
VIN Ripple current=4.7X1.7=7.99A(85°C)
-->故固態電容須2X7.99=15.98>11.45A

$$\begin{aligned} \text{Rocset} &= (\text{Iocp} * \text{Lgate}, \text{rdson}) / \text{Iocset} \\ \text{Rocset} &= (45\text{A} * 6.7\text{mOhm}) / 10\text{uA} = 30\text{K} \\ \text{Iocset} &= 10\text{uA} \end{aligned}$$

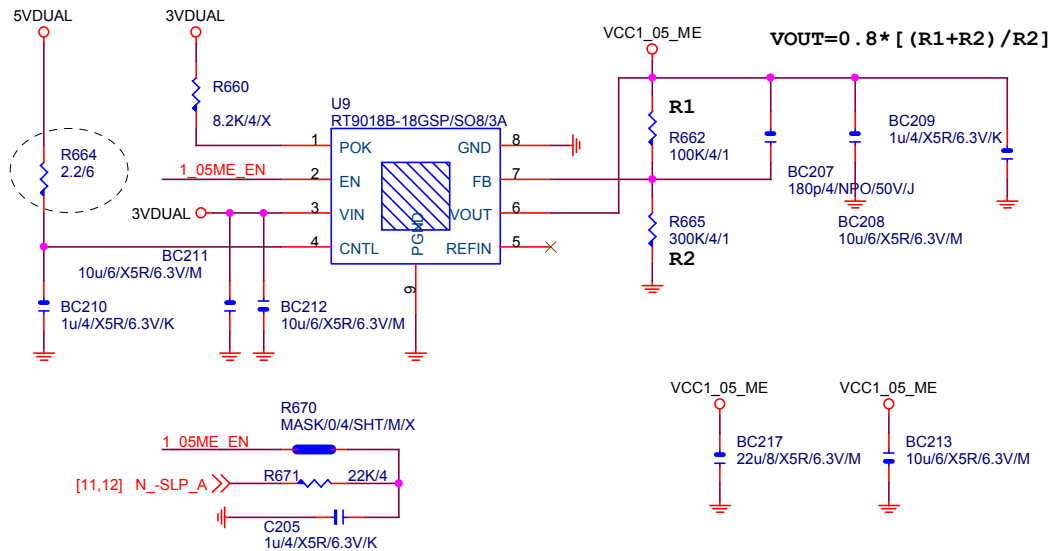
<i>Gigabyte Technology</i>			
Title			
DDR POWER			
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VCC1_05_ME

【技術通報R&D技術通報156】

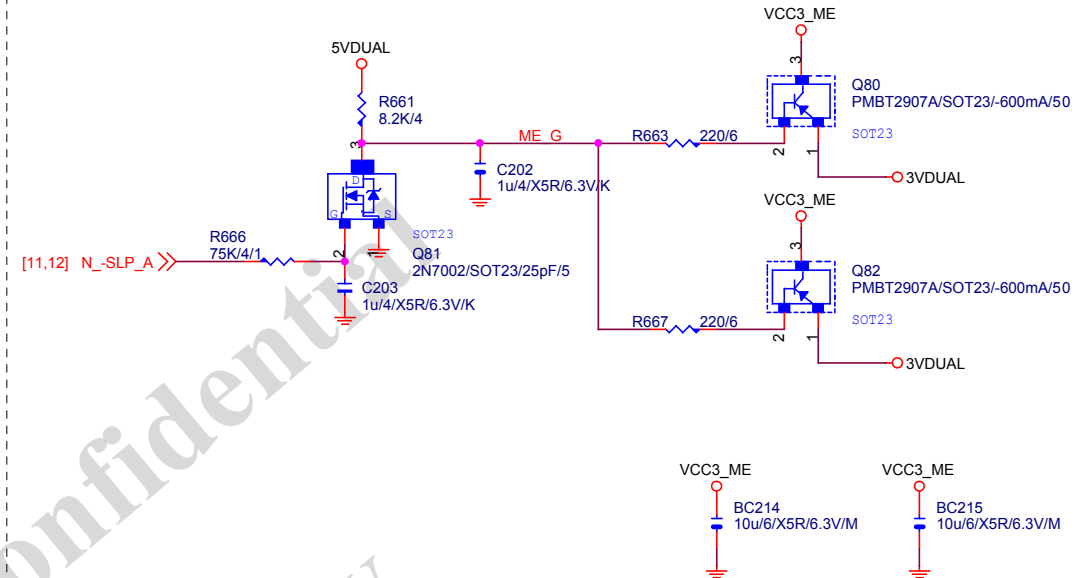
(RICHTER), (NUVOTON), (EMC) 做共用
PIN7分壓阻值須做修改為100K以上電阻值

Z97 N/A

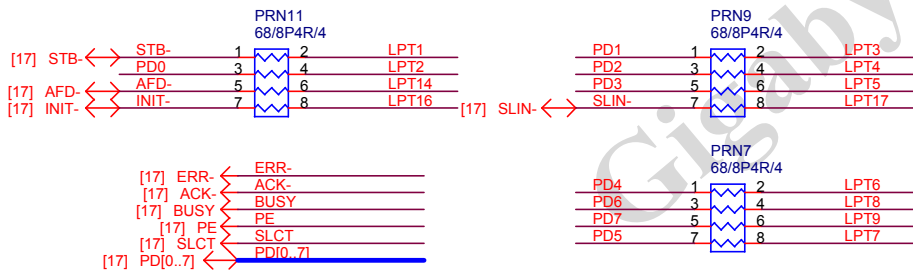


VCC3_ME

Z97 N/A

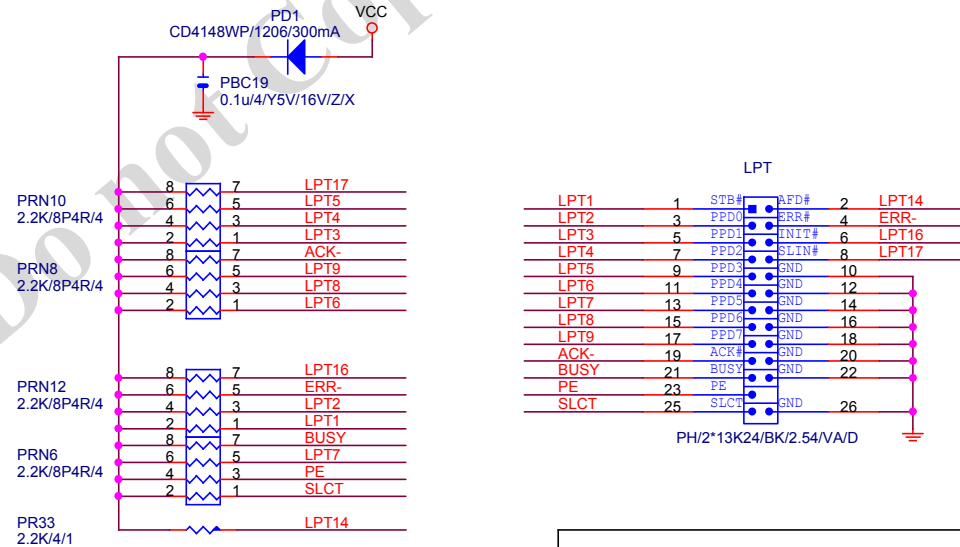


LPT PORT



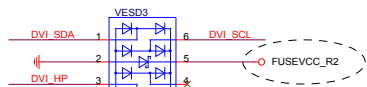
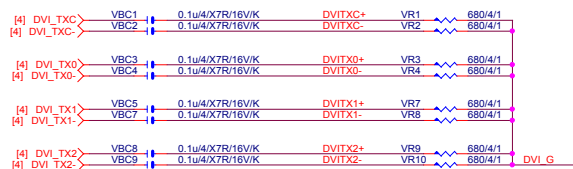
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33ohm Change to 68ohm

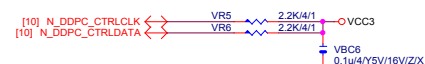


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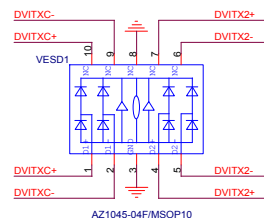
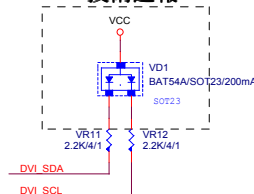
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DVI LEVEL SHIFT

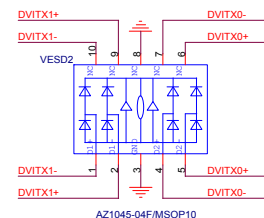
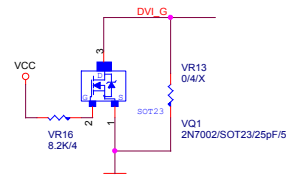
Close to connector



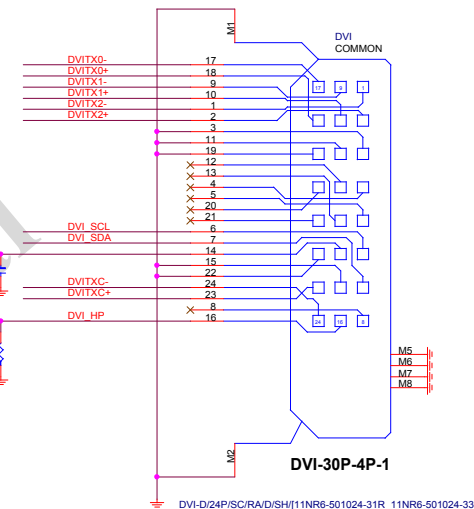
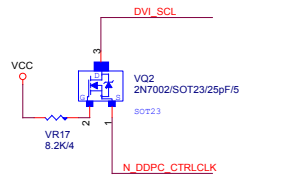
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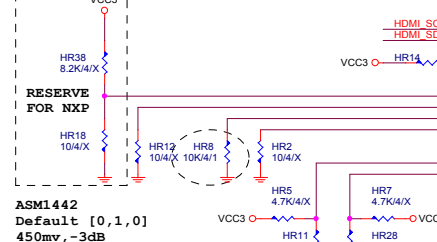
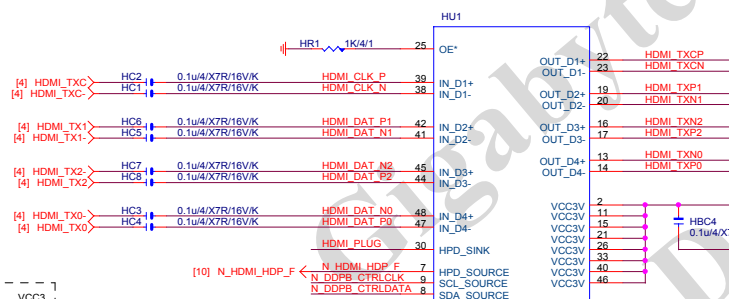
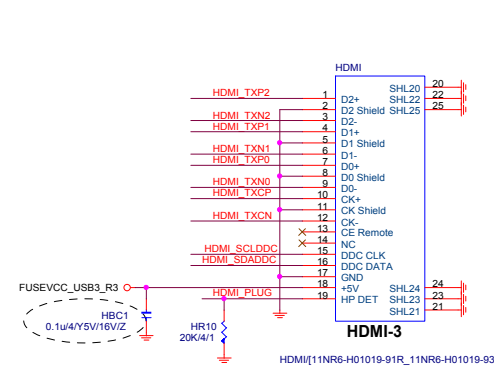
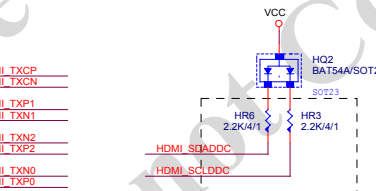
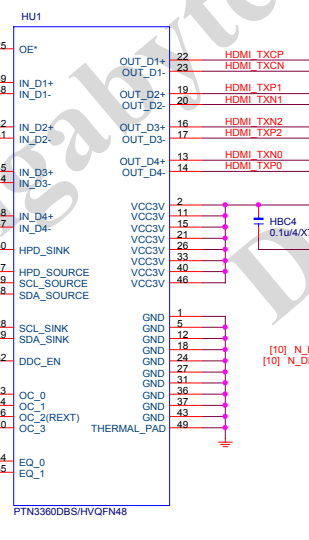
Close to connector



Close to connector



DVI-30P-4P-1

HDMI LEVEL SHIFTAS1442
Default [0,1,0]
450mv,-3dBAS1442 Default [0,0] 3dB
[0,1] 6dB

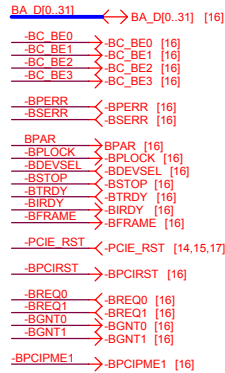
HDMI-3

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PCIE TO PCI

PCI: 5/4/5 Impedance=50 +- 15%



High: Enable PCI CLK 66MHz
Low: Disable PCI CLK 66MHz

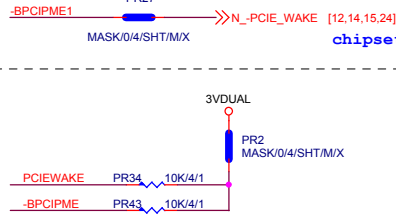


High: PCICLK INPUT form CLK Gen
Low: PCICLK OUTPUT form IT8893 chip

IT8892 PCI slot

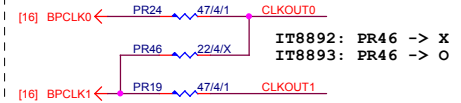


PCI slot chipset side

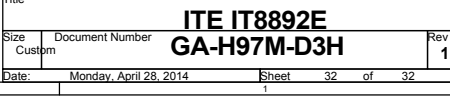
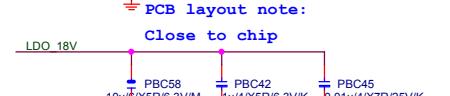
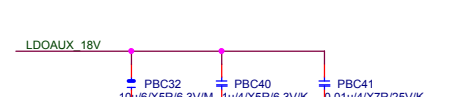
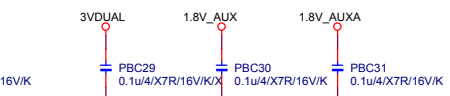
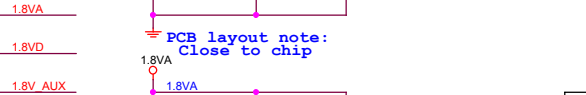
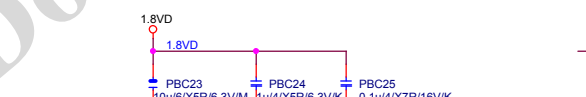
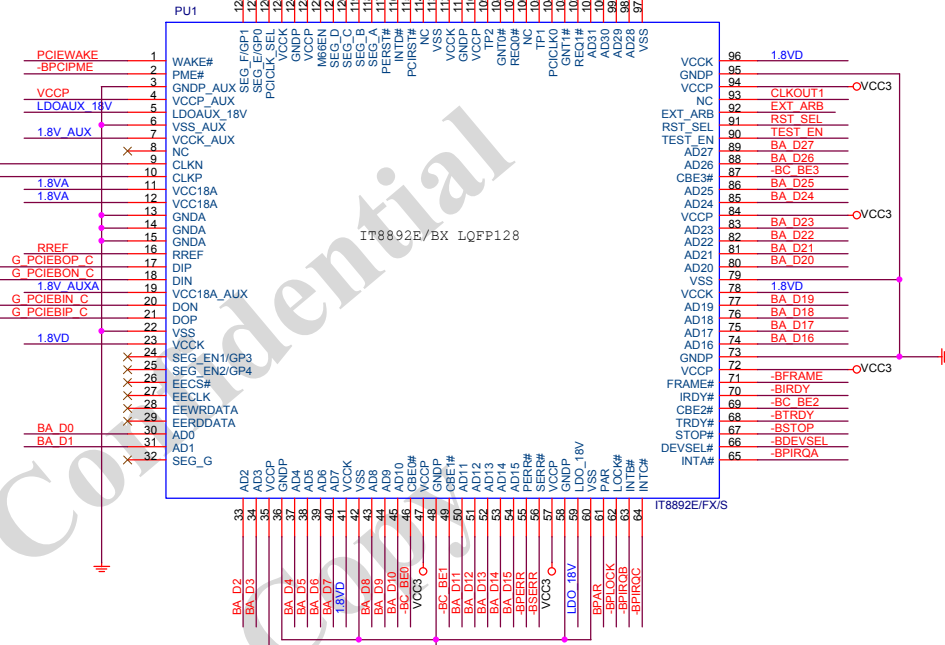
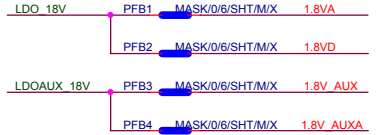
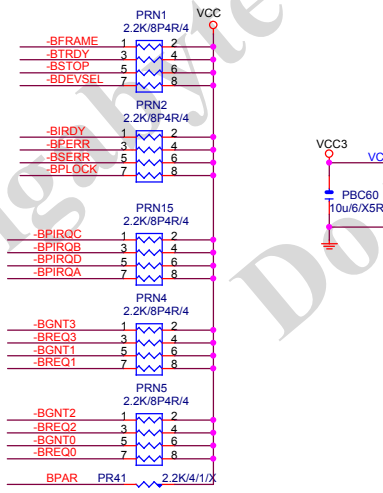
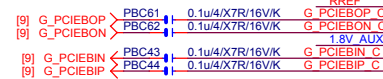
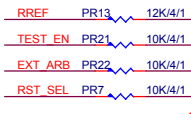


Co-Lay IT8893 (IT8893 CLKOUT1 N/A)

IT8892: PR24 -> 47ohm
IT8893: PR24 -> 22ohm



IT8892: PR19 -> O
IT8893: PR19 -> X



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